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PATENT

Docket No. 1232-4518

Express Mail Label No. EEE645143181US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**UTILITY APPLICATION AND APPLICATION FEE TRANSMITTAL (1.53(b))**

ASSISTANT COMMISSIONER FOR PATENTS  
Box Patent Application  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Named Inventor(s) and

Address(es):

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For:

Image Processing Apparatus, Its Control Method, and Storage Medium

Enclosed are:

[X] 73 page(s) of specification, 1 page(s) of Abstract, 15 page(s) of claims

[X] 12 sheets of drawing [X] formal [ ] informal

[X] 6 Page(s) of Declaration and Power of Attorney

[ ] Unsigned

[X] Newly Executed

[ ] Copy from prior application

[ ] Deletion of inventors including Signed Statement under 37 C.F.R. § 1.63(d)(2)

[X] Incorporation by Reference: The entire disclosure of the priority application(s) identified below, is considered as being part of the disclosure of the accompanying application and is incorporated herein by reference.

[ ] Microfiche Computer Program (Appendix)

[ ] \_\_\_\_\_ page(s) of Sequence Listing

[ ] computer readable disk containing Sequence Listing

[ ] Statement under 37 C.F.R. § 1.821(f) that computer and paper copies of the Sequence Listing are the same

[X] Claim for Priority Japanese Patent Application Nos 10-059612 filed 3/11/98 and 10-059613 filed 3/11/99

- [ ] English translation documents
- [ ] Information Disclosure Statement
- [ ] Copy of \_\_\_cited references w/ English Abstracts
- [ ] Copy of PTO-1449 filed in parent application serial No. \_\_\_\_\_
- [ ] Preliminary Amendment
- [X] Return receipt postcard (MPEP 503)
- [X] Assignment Papers (assignment cover sheet and assignment documents)
- [X] A check in the amount of \$40.00 for recording the Assignment.
- [ ] Assignment papers filed in parent application Serial No. \_\_\_\_\_
- [ ] Certification of chain of title pursuant to 37 C.F.R. § 3.73(b).
- [ ] This is a [ ] continuation [ ] divisional [ ] continuation-in-part (C-I-P) of prior application serial no. \_\_\_\_\_.
- [ ] Cancel in this application original claims \_\_\_\_\_ of the parent application before calculating the filing fee. (At least one original independent claim must be retained for filing purposes.)
- [ ] A preliminary Amendment is enclosed. (Claims added by this Amendment have been properly numbered consecutively beginning with the number following the highest numbered original claim in the prior application.)
- [ ] The status of the parent application is as follows:
- [ ] A Petition For Extension of Time and a Fee therefor has been or is being filed in the parent application to extend the term for action in the parent application until \_\_\_\_\_.
- [ ] A copy of the Petition for Extension of Time in the co-pending parent application is attached.
- [ ] No Petition For Extension of Time and Fee therefor are necessary in the co-pending parent application.
- [ ] Please abandon the parent application at a time while the parent application is pending or at a time when the petition for extension of time in that application is granted and while this application is pending has been granted a filing date, so as to make this application co-pending.
- [ ] Transfer the drawing(s) from the patent application to this application.
- [ ] Amend the specification by inserting before the first line the sentence:  
This is a [ ] continuation [ ] divisional [ ] continuation-in-part of co-pending application Serial No. \_\_\_\_\_ filed \_\_\_\_\_.

## I. CALCULATION OF APPLICATION FEE (For Other Than A Small Entity)

				Basic Fee
	Number Filed	Number Extra	Rate	\$ 760.00
Total Claims	33	-20=	13	x\$18.00 \$ 234.00
Independent Claims	18	- 3=	15	x78.00 \$1,170.00
Multiple Dependent Claims				
	[ ] yes		Additional Fee =	\$260.00
	[ X ] no		Add'l Fee =	NONE \$

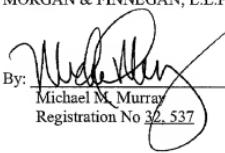
Total: \$2,164.00

- [ ] A statement claiming small entity status is attached or has been filed in the above-identified parent application and its benefit under 37 C.F.R. § 1.28(a) is hereby claimed. Reduced fees under 37 C.F.R. § 1.9(F) (50% of total) paid herewith \$ \_\_\_\_\_.
- [ X ] A check in the amount of \$2,164.00 for payment of the application filing fees is attached.
- [ ] Charge Fee(s) to Deposit Account No. 13-4500. Order No. \_\_\_\_\_. A DUPLICATE COPY OF THIS SHEET IS ATTACHED.
- [ X ] The Assistant Commissioner is hereby authorized to charge any additional fees which may be required for filing this application, or credit any overpayment to Deposit Account No. 13-4500, Order No. 1232-4518. A DUPLICATE COPY OF THIS SHEET IS ATTACHED.

Respectfully submitted,

MORGAN &amp; FINNEGAN, L.L.P.

By:


  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s) : Yoichi Yamagishi et al.  
Serial No. : TBA Group Art Unit : TBA  
Filed : March 5, 1999 (Herewith)  
For : Image Processing Apparatus, Its Control Method, and Storage Medium

**EXPRESS MAIL CERTIFICATE**

Express Mail Label No. EE645143178US

Date of Deposit March 5, 1999

I hereby certify that the following attached paper(s) and/or fee  
Application Fee Transmittal; 73 pp. of specs., 1 page of abstract, 15 pp. claims (33 TOTAL claims); Check  
in the amount of \$2,164.00 (Filing Fee); 6 pg. Executed Declaration/POA; 12 Sheets of Formal Drawings  
(Figs. 1-13); Assignment Recordation Form Cover Sheet w/ 2 pg. Executed Assignment; Check for \$40.00;  
and Return Postcard

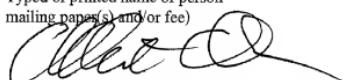
is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37

C.F.R. §1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Box New

Application, Washington, D.C. 20231.

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Typed or printed name of person  
mailing paper(s) and/or fee)



(Signature of person mailing  
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TITLE OF THE INVENTION

IMAGE PROCESSING APPARATUS, ITS CONTROL METHOD, AND  
STORAGE MEDIUM

5 BACKGROUND OF THE INVENTION

The present invention relates to an image processing apparatus for sensing, recording, and playing back still and moving images, its control method, and a 10 storage medium.

Conventionally, image processing apparatuses such as electronic cameras and the like, which record/play back still and moving images using memory cards having solid state memory elements as recording media have 15 already been commercially available, and electronic cameras with electronic viewfinders such as color liquid crystal panels and the like have also been commercially available.

These electronic cameras allow the electronic 20 camera user to determine the camera angle by continuously displaying images before sensing, and to confirm the sensed images by playing back and displaying them.

Especially, the function of playing back a sensed 25 image immediately after image sensing is very convenient and effective for the electronic camera user.

In such conventional image processing apparatus such as an electronic camera or the like, when an image is displayed on an electronic viewfinder such as a color liquid crystal panel or the like, since the color liquid 5 crystal panel consumes much electric power, the operable time and the number of images that can be sensed decrease considerably.

On the other hand, when image sensing is done using an optical viewfinder while turning off the 10 electronic viewfinder such as a color liquid crystal panel or the like, consumption power can be reduced, and the operable time and the number of images that can be sensed can be greatly increased. However, in such case, the sensed image cannot be played back and displayed 15 immediately after image sensing, resulting in inconvenience.

Also, in the conventional image processing apparatus such as an electronic camera or the like, in order to easily play back and display a sensed image 20 immediately after image sensing, sensed image data is converted into an image signal format suitable for image playback at an image display unit, and the converted signal is written in a recording medium.

For this reason, a combination of a luminance 25 signal Y and color difference signals R-Y and B-Y, a combination of R, G, and B signals, and the like are

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used in correspondence with the signal inputs of the image display unit, regardless of the pixel format of an image sensing means such as a CCD or the like. Even in an electronic camera that uses an image sensing means

- 5 with a unique pixel format, which can assure high image quality, when such camera has a function of playing back and displaying a sensed image immediately after image sensing, an image signal is not recorded in a format that can make most of the high image quality.

10 On the other hand, in case of an electronic camera which writes sensed image data in a recording medium in an image signal format corresponding to the pixel format of an image sensing means such as a CCD or the like, the recorded image quality can assure high performance of

15 the image sensing means. However, since this image signal format is not suitable for image playback at the image display unit, a sensed image cannot be directly played back and displayed.

20 SUMMARY OF THE INVENTION

The present invention has been made in consideration of such situation, and has as its object to provide an image processing apparatus which allows confirmation of the sensed image without considerably reducing the operable time and the number of images that

can be sensed of a camera, its control method, and a recording medium.

It is another object of the present invention to provide an image processing apparatus which can select

- 5 whether a sensed image is directly played back and displayed immediately after image sensing and is then recorded, or the sensed image is recorded while assuring high quality of that image, and is then played back and displayed, its control method, and a recording medium.

10 In order to solve the aforementioned problems and to achieve the above objects, an image processing apparatus of the present invention is characterized by the following arrangement according to its first aspect.

That is, there is provided an image processing  
15 apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: image sensing means, memory means, control means, and display means, wherein the control means stores an image signal output from the image  
20 sensing means in the memory means while the display means is disabled, enables the display means immediately after the storage, and plays back and displays the image signal stored in the memory means on the display means.

An image processing apparatus of the present  
25 invention is characterized by the following arrangement according to its second aspect.

That is, there is provided an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: image sensing means, memory means, 5 control means, and display means, wherein the control means stores an image signal output from the image sensing means in the memory means while the display means is disabled, enables the display means immediately after the storage, plays back and displays the image 10 signal stored in the memory means on the display means for a predetermined period of time, and then disables the display means.

An image processing apparatus of the present invention is characterized by the following arrangement 15 according to its third aspect.

That is, there is provided an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: image sensing means, memory means, 20 control means, display means, display mode setting means, and sense-and-play mode setting means, wherein when the display mode setting means is set to disable the display means, and the sense-and-play mode setting means is set to play back an image immediately after image sensing, 25 the control means stores an image signal output from the image sensing means in the memory means while the

display means is disabled, enables the display means immediately after the storage, and plays back and displays the image signal stored in the memory means on the display means.

- 5        An image processing apparatus of the present invention is characterized by the following arrangement according to its fourth aspect.

That is, there is provided an image processing apparatus for recording, and playing back and displaying  
10 a sensed still image and/or moving image on a recording medium, comprising: image sensing means, memory means, control means, display means, display mode setting means, sense-and-play mode setting means, and image sensing instruction means, wherein when the display mode setting means is set to disable the display means, and the  
15 sense-and-play mode setting means is set to play back an image immediately after image sensing, if the image sensing instruction means instructs start of image sensing, the control means stores an image signal output  
20 from the image sensing means in the memory means while the display means is disabled, and enables the display means immediately after the storage, and if the image sensing instruction means instructs end of image sensing, the control means disables the display means.

An image processing apparatus of the present invention is characterized by the following arrangement according to its fifth aspect.

- That is, there is provided an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: image sensing means, memory means, recording means, control means, display means, display mode setting means, and sense-and-play mode setting means, wherein when the display mode setting means is set to disable the display means, and the sense-and-play mode setting means is set to play back an image immediately after image sensing, the control means stores an image signal output from the image sensing means in the memory means while the display means is disabled, enables the display means immediately after the storage, plays back and displays the image signal stored in the memory means, and transfers the image signal stored in the memory means to record the image signal on the recording means.

An image processing apparatus of the present invention is characterized by the following arrangement according to its sixth aspect.

- That is, there is provided an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording

medium, comprising: image sensing means, memory means, recording means, control means, display means, display mode setting means, sense-and-play mode setting means, and image sensing instruction means, wherein when the  
5 display mode setting means is set to disable the display means, and the sense-and-play mode setting means is set to play back an image immediately after image sensing, if the image sensing instruction means instructs start of image sensing, the control means stores an image  
10 signal output from the image sensing means in the memory means while the display means is disabled, enables the display means immediately after the storage, plays back and displays the image signal stored in the memory means, and transfers the image signal stored in the memory  
15 means to record the image signal on the recording means, and if the image sensing instruction means instructs end of image sensing, the control means disables the display means.

A control method of an image processing apparatus  
20 of the present invention is characterized by the following arrangement according to its first aspect.

That is, there is provided a control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving  
25 image on a recording medium, comprising the steps of: storing an image signal output from image sensing means

in a memory while display means is disabled; enabling the display means immediately after the storage; and playing back and displaying the image signal stored in the memory means on the display means.

- 5        A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its second aspect.

That is, there is provided a control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of: storing an image signal output from image sensing means in a memory while display means is disabled; enabling the display means immediately after the storage; playing back and displaying the image signal stored in the memory means on the display means for a predetermined period of time; and then disabling the display means.

- 10      A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its third aspect.

That is, there is provided a control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of: 25 when display mode setting means is set to disable display means and sense-and-play mode setting means is

set to play back an image immediately after image sensing, storing an image signal output from image sensing means in a memory while the display means is disabled; enabling the display means immediately after 5 the storage; and playing back and displaying the image signal stored in the memory means on the display means.

A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its fourth aspect.

10 That is, there is provided a control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of: when display mode setting means is set to disable 15 display means and sense-and-play mode setting means is set to play back an image immediately after image sensing, if image sensing instruction means instructs start of image sensing, storing an image signal output from image sensing means in a memory while the display 20 means is disabled; enabling the display means immediately after the storage; and playing back and displaying the image signal stored in the memory means on the display means, and if the image sensing instruction means instructs end of image sensing, 25 disabling the display means.

A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its fifth aspect.

- That is, there is provided a control method of an
- 5 image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of: when display mode setting means is set to disable display means and sense-and-play mode setting means is
- 10 set to play back an image immediately after image sensing, storing an image signal output from image sensing means in a memory while the display means is disabled; enabling the display means immediately after the storage; playing back and displaying the image
- 15 signal stored in the memory means on the display means; and transferring the image signal stored in the memory means to record the image signal on recording means.

A control method of an image processing apparatus of the present invention is characterized by the

20 following arrangement according to its sixth aspect.

- That is, there is provided a control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of:
- 25 when display mode setting means is set to disable display means and sense-and-play mode setting means is

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set to play back an image immediately after image sensing, if image sensing instruction means instructs start of image sensing, storing an image signal output from image sensing means in a memory while the display  
5 means is disabled; enabling the display means immediately after the storage; playing back and displaying the image signal stored in the memory means on the display means; and transferring the image signal stored in the memory means to record the image signal in  
10 recording means, and if the image sensing instruction means instructs end of image sensing, disabling the display means.  
15

A storage medium of the present invention is characterized by the following arrangement according to  
15 its first aspect.

That is, there is provided a storage medium storing a program for implementing one of the above-mentioned control methods of an image processing apparatus.  
20

An image processing apparatus of the present invention is characterized by the following arrangement according to its seventh aspect.  
25

That is, there is provided an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: image sensing means, recording means,  
- 12 -

- and display means, and the apparatus having: a first recording mode for displaying an image signal output from the image sensing means on the display means, and then recording the image signal in the recording means
- 5       in an image signal display format; and a second recording mode for recording an image output from the image sensing means in the recording means in an image signal sense format, and then displaying the image signal on the display means.
- 10       An image processing apparatus of the present invention is characterized by the following arrangement according to its eighth aspect.
- That is, there is provided an image processing apparatus for recording, and playing back and displaying
- 15       a sensed still image and/or moving image on a recording medium, comprising: image sensing means, signal processing means, recording means, and display means, the image processing apparatus having: a first signal format corresponding to an input image signal of the
- 20       display means; and a second signal format corresponding to an output image signal of the image sensing means, and the apparatus further having: a first recording mode for displaying an image signal output from the image sensing means on the display means by converting the
- 25       image signal into the first signal format by the signal processing means, and then recording the image signal in

the recording means; and a second recording mode for recording an image output from the image sensing means in the recording means in the second signal format, and then displaying the image signal on the display means by 5 converting the image signal into the first signal format by the signal processing means.

An image processing apparatus of the present invention is characterized by the following arrangement according to its ninth aspect.

10 That is, there is provided an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: image sensing means, signal processing means, compression means, recording means, 15 and display means, the image processing apparatus having: a first signal format corresponding to an input image signal of the display means; and a second signal format corresponding to an output image signal of the image sensing means, and the apparatus further having: a 20 first recording mode for displaying an image signal output from the image sensing means on the display means by converting the image signal into the first signal format by the signal processing means, and then compressing the converted image signal by the 25 compression means and recording the compressed signal in the recording means; and a second recording mode for

recording an image signal output from the image sensing means in the second signal format, converting the recorded image signal into the first signal format by the signal processing means, and displaying the  
5 converted signal on the display means.

A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its seventh aspect.

That is, there is provided a control method of an  
10 image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: the first recording mode step of displaying an image signal output from image sensing means on display means, and then  
15 recording the image signal in recording means in an image signal display format; the second recording mode step of recording an image signal output from the image sensing means in the recording means in an image signal sense format, and then displaying the image signal on  
20 the display means; and the step of selecting one of the first and second recording mode steps.

A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its eighth aspect.

25 That is, there is provided a control method of an image processing apparatus for recording, and playing

back and displaying a sensed still image and/or moving image on a recording medium, comprising: the first recording mode step of displaying an image signal output from image sensing means on display means by converting  
5 the image signal into a first signal format corresponding to an input image signal of the display means, and recording the image signal in recording means; the second recording mode step of recording an image signal output from the image sensing means in the  
10 recording means in a second signal format corresponding to an output image signal of the image sensing means, and then displaying the image signal on the display means by converting the image signal into the first signal format; and the step of selecting one of the  
15 first and second recording mode steps.

A control method of an image processing apparatus of the present invention is characterized by the following arrangement according to its ninth aspect.

That is, there is provided a control method of an  
20 image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising: the first recording mode step of displaying an image signal output from image sensing means on display means by converting  
the image signal into a first signal format  
corresponding to an input image signal of the display  
25

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means, compressing the converted image signal, and recording the compressed image signal in recording means; the second recording mode step of recording an image signal output from the image sensing means in the 5 recording means in a second signal format corresponding to an output image signal of the image sensing means, converting the recorded image signal into the first signal format, and displaying the converted image signal on the display means; and the step of selecting one of 10 the first and second recording mode steps.

A storage medium of the present invention is characterized by the following arrangement according to its second aspect.

That is, there is provided a storage medium 15 storing a program for implementing one of the above-mentioned control methods of an image processing apparatus.

Other objects and advantages besides those discussed above shall be apparent to those skilled in 20 the art from the description of a preferred embodiment of the invention which follows. In the description, reference is made to accompanying drawings, which form a part hereof, and which illustrate an example of the invention. Such example, however, is not exhaustive of 25 the various embodiments of the invention, and therefore

reference is made to the claims which follow the description for determining the scope of the invention.

#### BRIEF DESCRIPTION OF THE DRAWINGS

5        Fig. 1 is a block diagram showing the arrangement according to the first embodiment of the present invention;

Fig. 2 is a flow chart showing a main routine of the first embodiment;

10      Fig. 3 is a flow chart showing the main routine of the first embodiment;

Fig. 4 is a flow chart showing a distance measurement/photometry process in detail;

15      Fig. 5 is a flow chart showing an image sensing process in detail;

Fig. 6 is a flow chart showing a recording process in detail;

Fig. 7 is a block diagram showing the arrangement according to the second embodiment of the present invention;

20      Fig. 8 is a flow chart showing a main routine of the second embodiment;

Fig. 9 is a flow chart showing the main routine of the second embodiment;

25      Fig. 10 is a flow chart showing the main routine of the second embodiment;

Fig. 11 is a flow chart showing an image sensing process in detail;

Fig. 12 is a flow chart showing a display image process in detail; and

5 Fig. 13 is a flow chart showing a compression process in detail.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The preferred embodiments of the present invention  
10 will be described in detail hereinafter with reference to the accompanying drawings.

Note that the present invention can be practiced not only in the form of an image processing apparatus but also in the form of a control method of an image  
15 processing apparatus, and a storage medium such as a CD-ROM or the like that stores a program for implementing this control method.

(First Embodiment)

Fig. 1 is a block diagram showing the arrangement  
20 of an "image processing apparatus" according to the first embodiment. Referring to Fig. 1, reference numeral 100 denotes an image processing apparatus. Reference numeral 10 denotes a photographing lens; 12, a shutter with a stop function; 14, an image sensing element for  
100  
25 converting an optical image into an electrical signal; and 16, an A/D converter for converting an analog signal

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

output from the image sensing element 14 into a digital signal.

Reference numeral 18 denotes a timing generation circuit for supplying clock signals and control signals 5 to the image sensing element 14, the A/D converter 16, and a D/A converter 26. The timing generation circuit 18 is controlled by a memory control circuit 22 and system control circuit 50.

Reference numeral 20 denotes an image processing 10 circuit which performs predetermined pixel interpolation and color conversion for data supplied from the A/D converter 16 or the memory control circuit 22. The image processing circuit 20 makes a predetermined computation using sensed image data to execute a TTL (Through The 15 Lens) AF (auto focus) process, AE (auto exposure) process, and EF (flash pre-emission) process in which the system control circuit 50 controls an exposure control means 40 and distance measurement control means 42 on the basis of the obtained computation result. 20 Furthermore, the image processing circuit 20 makes a predetermined computation using sensed image data, and executes a TTL AWB (auto white balance) process on the basis of the obtained computation result.

Reference numeral 22 denotes the memory control 25 circuit, which controls the A/D converter 16, the timing generation circuit 18, the image processing circuit 20,

an image display memory 24, the D/A converter 26, a memory 30, and a compression/expansion circuit 32.

Data output from the A/D converter 16 is written in the image display memory 24 or memory 30 via the 5 image processing circuit 20 and memory control circuit 22 or directly via the memory control circuit 22.

Reference numeral 24 denotes the image display memory; 26, the D/A converter; and 28, an image display unit comprising a TFT LCD or the like. Display image 10 data written in the image display memory 24 is displayed by the image display unit 28 via the D/A converter 26. When sensed image data are displayed at image sensing timings using the image display unit 28, an electronic viewfinder function can be implemented. The image 15 display unit 28 can arbitrarily turn on/off its display in response to an instruction from the system control circuit 50. When the display is turned off, the consumption power of the image processing apparatus 100 can be greatly reduced.

20 Reference numeral 30 denotes the memory for storing a still image and moving image. The memory 30 has a storage capacity large enough to store a predetermined number of still images or a moving image for a predetermined period of time. With this 25 arrangement, even in a continuous or panoramic image sensing mode for continuously sensing a plurality of

still images, many images can be written in the memory 30 at high speed. Also, the memory 30 can be used as a work area of the system control circuit 50.

- Reference numeral 32 denotes the
- 5 compression/expansion circuit for compressing/expanding image data by adaptive discrete cosine transformation (ADCT) or the like. The compression/expansion circuit 32 reads an image stored in the memory 30, compresses or expands it, and writes the processed data in the memory  
10 30.

Reference numeral 40 denotes the exposure control means for controlling the shutter 12 with the stop function. The exposure control means 40 also has a flash light control function in cooperation with an electronic  
15 flash 48.

Reference numeral 42 denotes the distance measurement control means for controlling focusing of the photographing lens 10; 44, a zoom control means for controlling zooming of the photographing lens; and 46, a  
20 barrier control means for controlling operation of a protection means 102 serving as a barrier.

Reference numeral 48 denotes the electronic flash which has a function of projecting AF auxiliary light and the flash light control function.

25 The exposure control means 40 and distance measurement control means 42 are controlled using TTL,

and the system control circuit 50 controls the exposure control means 40 and distance measurement control means 42 on the basis of the computation result of the image processing circuit 20 using sensed image data.

- 5        Reference numeral 50 denotes the system control circuit for controlling the overall image processing apparatus 100; and 52, a memory for storing constants, variables, programs, and the like for operations of the system control circuit 50.
- 10      Reference numeral 54 denotes an indication unit which includes a liquid crystal display device, loudspeaker, and the like, and indicates operation states, messages, and the like using characters, images, voices, and the like in accordance with execution of a 15 program by the system control circuit 50. The indication unit 54 is placed at one or a plurality of easy-to-see positions around the console of the image processing apparatus 100, and is comprised of a combination of an LCD, LEDs, tone generation element, and the like. Some 20 functions of the indication unit 54 are placed within an optical viewfinder 104.

Of the indication contents of the indication unit 54, those indicated on an LCD or the like include, e.g., a single shot/continuous shot indication, self-timer 25 indication, compression ratio indication, recording pixel count indication, recording image count indication,

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remaining photographable image count indication, shutter speed indication, aperture value indication, exposure correction indication, flash indication, red-eye suppression indication, macro-image sensing indication,

- 5      buzzer setting indication, timepiece battery remaining capacity indication, error indication, information indication using a plurality of digits of numerals, loading/unloading state indication of recording media 200 and 210, communication I/F operation indication,
- 10     date/time indication, indication that indicates a connection state with an external computer, and the like.

      Of the indication contents of the indication unit 54, those indicated within the optical viewfinder 104 include, e.g., an in-focus indication, image sensing

- 15     ready indication, camera shake alert indication, flash charging indication, flash charging completion indication, shutter speed indication, aperture value indication, exposure correction indication, recording medium write access indication, and the like.

- 20     Furthermore, of the indication contents of the indication unit 54, those indicated by LEDs or the like include, e.g., in-focus indication, image sensing ready indication, camera shake alert indication, flash charging indication, flash charging completion indication, recording medium write access indication,
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macro-image sensing setting notification indication, secondary battery charged state indication, and the like.

Of the indication contents of the indication unit 54, those to be indicated by lamps include, e.g., a 5 self-timer notification lamp, and the like. The self-timer notification lamp may be commonly used as that for emitting AF auxiliary light.

Reference numeral 56 denotes a rewritable nonvolatile memory, which uses, e.g., an EEPROM.

10 Reference numerals 60, 62, 64, 66, 68, and 70 denote operation means for inputting various operation instructions of the system control circuit 50. These operation means are constructed by one or a plurality of combinations of a switch, a dial, a touch panel, a 15 pointer using line of sight detection, a voice recognition device, and the like.

An example of these operation means will be explained below. Reference numeral 60 denotes a mode dial switch, which can switch various function modes 20 such as power OFF, an automatic image sensing mode, image sensing mode, panoramic image sensing mode, playback mode, multi-frame playback/erase mode, PC connection mode, and the like.

Reference numeral 62 denotes a shutter switch SW1, 25 which is turned on in the middle of operation (at the half stroke position) of a shutter button (not shown),

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and instructs start of an AF (auto focus) process, AE (auto exposure) process, AWB (auto white balance) process, EF (flash pre-emission) process, and the like.

- Reference numeral 64 denotes a shutter switch SW2, 5 which is turned on upon completion of operation (at the full stroke position) of the shutter button (not shown), and instructs start of a series of processes including an exposure process for writing a signal read out from the image sensing element 14 as image data in the memory 10 30 via the A/D converter 16 and memory control circuit 22, a development process using computation results in the image processing circuit 20 and memory control circuit 22, and a recording process for reading out image data from the memory 30, compressing the readout 15 data by the compression/expansion circuit 32, and writing the compressed image data in the recording medium 200 or 210.

- Reference numeral 66 denotes an image display ON/OFF switch, which can set an ON/OFF state of the 20 image display unit 28. With this function, when current supply to the image display unit 28 comprising the TFT LCD or the like is cut off upon image sensing using the optical viewfinder 104, power savings can be attained.

- Reference numeral 68 denotes a quick review ON/OFF 25 switch which sets a quick review function that automatically plays back sensed image data immediately

after image sensing. Note that this embodiment especially has a function of setting the quick review function when the image display unit 28 is turned off.

- Reference numeral 70 denotes a console including  
5 various buttons, touch panel, and the like, which include a menu button, set button, macro button, multi-frame playback new page button, flash setting button, single shot/continuous shot/self-timer switch button, menu movement + (plus) button, menu movement 10 - (minus) button, playback image movement + (plus) button, playback image movement - (minus) button, sensed image quality selection button, exposure correction button, date/time setting button, and the like.

- Reference numeral 80 denotes a power supply  
15 control means, which is comprised of a battery detection circuit, a DC-DC converter, a switch circuit for switching a block to be energized, and the like. The power supply control means 80 detects the presence/absence, type, and remaining battery amount of 20 a battery attached, controls the DC-DC converter on the basis of such detection results and an instruction from the system control circuit 50, and supplies a required voltage to the respective units including the recording medium for a required period of time.

- 25 Reference numerals 82 and 84 denote connectors; and 86, a power supply means including a primary battery

such as an alkali battery, lithium battery, or the like, a secondary battery such as an NiCd battery, NiMH battery, Li Battery, or the like, an AC adapter, and the like.

- 5        Reference numerals 90 and 94 denote interfaces with recording media such as a memory card, hard disk, and the like; 92 and 96, connectors for connecting the recording media such as a memory card, hard disk, and the like; and 98, a recording medium
- 10      attachment/detachment detection means for detecting whether or not the recording medium 200 or 210 is attached to the connector 92 and/or the connector 96.

Note that this embodiment has two sets of interfaces and connectors that receive the recording media. Of course, the number of sets of interfaces and connectors that receive the recording media is not particularly limited. Also, combinations of interfaces and connectors of different standards may be used. As the interface and connector, those complying with the standards of a PCMCIA card, CF (compact flash) card, and the like may be used.

Furthermore, when the interfaces 90 and 94, and connectors 92 and 96 use those complying with the standards of a PCMCIA card, CF (compact flash) card, and the like, various communication cards such as a LAN card, modem card, USB card, IEEE1394 card, P1284 card, SCSI

card, PHS, and the like are connected thereto, image data and associated management information can be transferred between the image processing apparatus 100 and an external computer or its peripheral devices such as a printer and the like.

Reference numeral 102 denotes the protection means serving as a barrier which covers an image sensing unit including the lens 10 of the image processing apparatus 100 to protect it from contamination and damages.

Reference numeral 104 denotes the optical viewfinder, which allows image sensing using the optical viewfinder alone without using the electronic viewfinder function implemented by the image display unit 28. In the optical viewfinder 104, some functions of the indication unit 54, e.g., an in-focus indication, camera shake alert indication, flash charging indication, shutter speed indication, aperture value indication, exposure correction indication, and the like are placed.

Reference numeral 110 denotes a communication means having various communication functions such as RS232C, USB, IEEE1394, PI284, SCSI, modem, LAN, radio communication, and the like.

Reference numeral 112 denotes a connector or antenna (in case of a radio communication), which connects the image processing apparatus 100 to another device.

C O M P U T E R S Y S T E M S

Reference numeral 200 denotes the recording medium such as a memory card, hard disk, or the like. The recording medium 200 comprises a recording unit 202 comprised of a semiconductor memory, magnetic disk, or 5 the like, an interface 204 with the image processing apparatus 100, and a connector 206 for connecting the image processing apparatus 100.

Reference numeral 210 denotes the recording medium such as a memory card, hard disk, or the like. The 10 recording medium 210 comprises a recording unit 212 comprised of a semiconductor memory, magnetic disk, or the like, an interface 214 with the image processing apparatus 100, and a connector 216 for connecting the image processing apparatus 100.

15 The operation of this embodiment will be explained below with reference to Figs. 2, 3, 4, 5, and 6.

Figs. 2 and 3 are flow charts showing the main routine of the image processing apparatus 100 of this embodiment. The operation of the image processing 20 apparatus 100 will be described with the aid of Figs. 2 and 3.

Upon power ON after battery exchange or the like, the system control circuit 50 initializes flags, control variables, and the like (S101), and resets image display 25 of the image display unit 28 to an OFF state (S102).

- The system control circuit 50 checks the setting position of the mode dial 60. If the mode dial 60 is set at a power OFF position (S103), the system control circuit 50 executes a predetermined end process (S105).
- 5 More specifically, the system control circuit 50 changes the indications of the respective indication units to an end state, protects the image sensing unit by closing the barrier of the protection means 102, records required parameters and setting values including flags, 10 control variables, and the like, and setting mode in the nonvolatile memory 56, cuts off unnecessary power supply to the respective units of the image processing apparatus 100 including the image display unit 28 by the power supply control means 80, and so forth. After that, 15 the flow returns to step S103.

On the other hand, if the mode dial 60 is set at an image sensing mode position (S103), the flow advances to step S106.

- If the mode dial is set at any of other mode 20 positions (S103), the system control circuit 50 executes a process corresponding to the selected mode (S104), and the flow returns to step S103 upon completion of the process.

The system control circuit 50 checks using the 25 power supply control means 80 if the remaining capacity and operation state of the power supply means 86

comprising batteries and the like pose any problem in  
the operation of the image processing apparatus 100  
(S106). If a problem is found, a predetermined alert  
indication is made by means of an image and voice using  
5 the indication unit 54 (S108), and the flow then returns  
to step S103.

If no problem is found in the power supply means  
86 (S106), the system control circuit 50 checks if the  
operation state of the recording medium 200 or 210 poses  
10 any problem in the operation of the image processing  
apparatus 100, in particular, recording/playback of  
image data to/from the recording medium (step S107). If  
any problem is found, a predetermined alert indication  
is made by means of an image and voice using the  
15 indication unit 54 (S108), and the flow then returns to  
step S103.

If no problem is found in the operation state of  
the recording medium 200 or 210 (S107), various setting  
states of the image display apparatus 100 are indicated  
20 by means of an image and voice using the indication unit  
54 (S109). When the image display of the image display  
unit 28 is ON, various setting states of the image  
display apparatus 100 are indicated by means of an image  
and voice also using the image display unit 28.

25 The system control circuit 50 checks the setting  
state of the quick review ON/OFF switch 68 (S110). If

the quick review mode is ON, the system control circuit 50 sets a quick review flag (S111); otherwise, it resets the quick review flag (S112).

- Note that the status of the quick review flag is  
5 stored in an internal memory of the system control  
circuit 50 or the memory 52.

- Subsequently, the system control circuit 50 checks  
the setting state of the image display ON/OFF switch 66  
(S113). If the image display mode is ON, the system  
10 control circuit 50 sets an image display flag (S114),  
turns on the image display of the image display unit 28  
(S115), and sets a through display state for displaying  
sensed image data in turn (S116). The flow then advances  
to step S119 (see Fig. 3).

- 15 In the through display state, data written in the  
image display memory 24 via the image sensing element 14,  
A/D converter 16, image processing circuit 20, and  
memory control circuit 22 are displayed on the image  
display unit 28 via the memory control circuit 22 and  
20 D/A converter 26, thus implementing the electronic  
viewfinder function.

- If the image display ON/OFF switch 66 is set in an  
image display OFF mode (S113), the system control  
circuit 50 resets the image display flag (S117), and  
25 turns off the image display of the image display unit 28  
(S118). Then, the flow advances to step S119.

When the image display is OFF, image sensing is done using the optical viewfinder 104 without using the electronic viewfinder function implemented by the image display unit 28. In this case, the power consumption of 5 the image display unit 28, D/A converter 26, and the like, which consume large power, can be reduced.

Note that the status of the image display flag is stored in the internal memory of the system control circuit 50 or the memory 52.

10 If the shutter switch SW1 has not been pressed yet (S119), the flow returns to step S103.

If the shutter switch SW1 has been pressed (S119), the system control circuit 50 checks the status of the image display flag stored in its internal memory or the 15 memory 52 (S120). If the image display flag is set, the system control circuit 50 sets the display state of the image display unit 28 in a freeze display state (S121), and the flow then advances to step S122.

In the freeze display state, image data on the 20 image display memory 24 is inhibited from being rewritten via the image sensing element 14, A/D converter 16, image processing circuit 20, and memory control circuit 22, and the latest written image data is displayed on the image display unit 28 via the memory 25 control circuit 22 and D/A converter 26, thus displaying a frozen image on the electronic viewfinder.

If the image display flag is reset (S120), the flow advances to step S122.

The system control circuit 50 executes a distance measurement to adjust the focus of the photographing

- 5 lens 10 on an object, and determines the aperture value and shutter speed by photometry (S122). In photometry, the electronic flash is set up if necessary:

The distance measurement/photometry process (S122) will be described in detail later with reference to

10 Fig. 4.

Upon completion of the distance measurement/photometry process (S122), the system control circuit 50 checks the status of the image display flag stored in its internal memory or the memory

- 15 52 (S123). If the image display flag is set, the system control circuit 50 sets the display state of the image display unit 28 in a through display state (S124), and the flow then advances to step S125. Note that the through display state in step S124 is the same operation  
20 state as that in step S116 (see Fig. 2).

If the shutter switch SW2 is not pressed (S125) and the shutter switch SW1 is canceled (S126), the flow returns to step S103.

- 25 If the shutter switch SW2 has been pressed (S125), the system control circuit 50 checks the status of the image display flag stored in its internal memory or the

memory 52 (S127). If the image display flag is set, the system control circuit 50 sets the display state of the image display unit 28 in a fixed color display state (S128), and the flow then advances to step S129.

- 5        In the fixed color display state, in place of sensed image data, which has been written in the image display memory 24 via the image sensing element 14, A/D converter 16, image processing circuit 20, and memory control circuit 22, substitute fixed-color image data is  
10      displayed on the image display unit 28 via the memory control circuit 22 and D/A converter 26, thus displaying a fixed-color image on the electronic viewfinder 28.

If the image display flag is reset (S127), the flow advances to step S129.

- 15      The system control circuit 50 executes an image sensing process including an exposure process for writing sensed image data in the memory 30 via the image sensing element 14, A/D converter 16, image processing circuit 20, and memory control circuit 22 or directly  
20      from the A/D converter via the memory control circuit 22, and a development process for reading out image data written in the memory 30 and executing various processes of the readout image data using the memory control circuit 22 and the image processing circuit 20 if  
25      required (S129).

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The image sensing process (S129) will be described in detail later with reference to Fig. 5.

The system controller 50 checks the status of the image display flag stored in its internal memory or the  
5 memory 52 (S130). If the image display flag is set, the system control circuit 50 performs a quick review display (S133). In this case, the image display unit 28 always displays an image as an electronic viewfinder even during image sensing, and a quick review display  
10 immediately after image sensing is also made.

If the image display flag is reset (S130), the system control circuit 50 checks the status of the quick review flag stored in its internal memory or the memory 52 (S131). If the quick review flag is set, the system  
15 control circuit 50 turns on the image display of the image display unit 28 (S132) to make a quick review display (S133).

In this case, even when the image display of the image display unit 28 is turned off to achieve power savings or to make image sensing using the optical viewfinder 104 without requiring any electronic viewfinder function, if the quick review function is set by the quick review switch, a sensed image can be automatically played back on the image display unit 28  
25 immediately after image sensing, thus providing a

function that is convenient for power savings and confirmation of the sensed image.

- If the image display flag is reset (S130) and the quick review flag is also reset (S131), the flow
- 5 advances to step S134 while the image display unit 28 is kept OFF. In this case, even after image sensing, the image display unit 28 is kept OFF, and no quick review display is made. This method is used when the sensed image need not be confirmed immediately after image
- 10 sensing, and it is more important to attain power savings without using the electronic viewfinder function of the image display unit 28 like in a case wherein the user continues image sensing using the optical viewfinder 104.
- 15 The system control circuit 50 reads out sensed image data written in the memory 30, and executes various image processes using the memory control circuit 22 and the image processing circuit 20 if required, and image compression corresponding to the selected mode
- 20 using the compression/expansion circuit 32. The system control circuit 50 then executes a recording process for writing image on the recording medium 200 or 210 (S134).
- The recording process (S134) will be described in detail later with reference to Fig. 6.
- 25 Upon completion of the recording process (S134), if the shutter switch SW2 is kept pressed (S135), the

system control circuit 50 checks the status of a continuous shot flag stored in its internal memory or the memory 52 (S136). If the continuous shot flag is set, the flow returns to step S129 to continuously make image sensing, and the next image sensing is made.

If the continuous shot flag is not set (S136), the current process repeats itself until the shutter switch SW2 is canceled.

In this manner, according to this embodiment, in an operation setup state that makes the quick review display immediately after image sensing, if the shutter switch SW2 is kept pressed upon completion of the recording process (S134), the quick review display on the image display unit 28 can continue until the shutter switch SW2 is released, and the user can carefully check the sensed image.

If the shutter switch SW2 is not pressed upon completion of the recording process (S134), or if the shutter switch SW2 is canceled after the sensed image is checked by holding down the shutter switch SW2 to continue the quick review display (S135), the flow advances to step S138 after an elapse of a predetermined minimum review time (S137).

As described above, according to this embodiment, since the quick review display on the image display unit 28 continues for a predetermined period of time, the

user can reliably confirm the sensed image, and can be prevented from losing the next image sensing chance by continuing the quick review display for an unnecessarily long period of time.

5 Note that the setting method of this minimum review time is not particularly limited. That is, the minimum review time may be set at a fixed value, may be arbitrarily set by the user, or may be arbitrarily set or selected by the user within a predetermined range.

10 If the system control circuit 50 determines that the image display flag is set (S138), it sets the display state of the image display unit 28 in the through display state (S139), and the flow advances to step S141.

15 In this case, after the sensed image is confirmed by the quick review display on the image display unit 28, the through display state can be set to display sensed image data in turn for the next image sensing.

If the image display flag is reset (S138), the  
20 system control circuit 50 turns off the image display of the image display unit 28 (S140), and the flow advances to step S141.

In this case, after the sensed image is confirmed by the quick review display on the image display unit 28,  
25 the function of the image display unit 28 may be disabled to attain power savings, i.e., the consumption

power of the image display unit 28, D/A converter 26, and the like that require large power can be reduced.

If the shutter switch SW1 has been pressed (S141), the flow returns to step S125 and the system control circuit 50 prepares for the next image sensing.

On the other hand, if the shutter switch SW1 is canceled (S141), the system control circuit 50 ends a series of image sensing operations, and the flow returns to step S103.

Fig. 4 is a flow chart showing the distance measurement/photometry process in step S122 in Fig. 3 in detail.

The system control circuit 50 reads out a charge signal from the image sensing element 14, and the image processing circuit 20 sequentially reads sensed image data via the A/D converter 16 (S201). Using the read image data, the image processing circuit 20 makes predetermined computations used in the TTL AE (auto exposure), EF (flash pre-emission) process, and AF (auto focus) process. Note that the respective processes extract specific portions at required positions from all pixels, and use them in computations. In this manner, in the TTL AE, EF, AWB, and AF processes, optimal computations can be made in units of different modes, i.e., a center-weighted mode, average mode, evaluation mode, and the like.

The system control circuit 50 executes AE control using the exposure control means 40 using the computation results in the image processing circuit 20 (S203) until it is determined that the exposure value 5 (AE) is appropriate (S202).

10 The system control circuit 50 checks using measurement data obtained by the AE control if flash emission is required (S204). If flash emission is required, the system control circuit 50 sets a flash flag, and charges the electronic flash 48 (S205).

If it is determined that the exposure value (AE) is appropriate (S202), the system control circuit 50 stores the measurement data and/or setting parameters in its internal memory or the memory 52.

15 Using the computation results in the image processing circuit 20 and the measurement data obtained by the AE control, the system control 50 executes AWB control by adjusting the color processing parameters using the image processing circuit 20 (S207) until it is 20 determined that the white balance (AWB) is appropriate (S206).

If it is determined that the white balance (AWB) is appropriate (S206), the system control circuit 50 stores the measurement data and/or setting parameters in 25 its internal memory or the memory 52.

Using the measurement data obtained by the AE control and AWB control, the system control circuit 50 executes AF control using the distance measurement control means 42 (S209) until it is determined that the 5 distance measurement (AF) result indicates in-focus (S208).

If it is determined that the distance measurement (AF) result indicates in-focus (S208), the system control circuit 50 stores the measurement data and/or 10 setting parameters in its internal memory or the memory 52, thus ending the distance measurement/photometry process routine (S122).

Fig. 5 is a flow chart showing the image sensing process in step S129 in Fig. 3 in detail.

15 The system control circuit 50 opens the shutter 12 having the stop function using the exposure control means 40 in accordance with photometry data stored in its internal memory or the memory 52 to expose the image sensing element 14 (S301, S302).

20 The system control circuit 50 checks based on the flash flag if the electronic flash 48 is required (S303). If the electronic flash 48 is required, the system control circuit 50 controls the electronic flash to emit light (S304).

25 The system control circuit 50 waits for completion of exposure of the image sensing element 14 in

accordance with the photometry data (S305), closes the shutter 12 (S306), and reads a charge signal from the image sensing element 14 and writes sensed image data in the memory 30 via the A/D converter 16, image processing circuit 20, and memory control circuit 22 or directly from the A/D converter 16 via the memory control circuit 22 (S307).

If a frame process is required in correspondence with the selected image sensing mode (S308), the system control circuit 50 reads out image data written in the memory 30, executes a vertical addition process (S309) and color process (S310) in turn using the memory control circuit 22 and image processing apparatus 20 if required, and writes the processed image data in the memory 30.

The system control circuit 50 reads out image data from the memory 30, and transfers display image data to the image display memory 24 via the memory control circuit 22 (S311).

Upon completion of a series of processes, the image sensing process routine (S129) ends.

Fig. 6 is a flow chart showing the recording process in step S134 in Fig. 3 in detail.

The system control circuit 50 executes a pixel squaring process for reading out sensed image data written in the memory 30, and converting the

C O R R E C T E D  
S E C U N D A R Y  
P R I M A R Y

vertical-to-horizontal pixel ratio of the image sensing element to 1 : 1 by interpolation using the memory control circuit 22 and image processing circuit 20 if required (S401). The system control circuit 50 then

5 writes the processed image data in the memory 30.

The system control circuit 50 reads out image data written in the memory 30, and executes image compression corresponding to the selected mode using the compression/expansion circuit 32 (S402). After that, the

10 system control circuit 50 writes the compressed image data in the recording medium 200 or 210 such as a memory card, compact flash card, or the like via the interface 90 or 94, and the connector 92 or 96 (S403).

Upon completion of a write in the recording medium, 15 the recording process routine (S134) ends.

The first embodiment of the present invention has been explained using Fig. 1.

In the description of the embodiment, when the image display unit 28 is turned off, all building

20 components of the image display unit 28 are turned off, but only some building components of the image display unit 28 may be turned off. In such case, a function that can attain both confirmation of the sensed image and power savings can be implemented. For example, when the

25 image display unit 28 is comprised of an LCD and backlight, the backlight alone may be turned off while

the LCD is kept ON, thus implementing the function that can attain both confirmation of the sensed image and power savings as in the description of the embodiment.

- The image display ON/OFF switch 66 and quick review ON/OFF switch 68 are independently equipped. However, the image display ON/OFF switch 66 and quick review ON/OFF switch 68 may be combined into a single, common switch, and that switch may have three positions, i.e., image display ON/image display ON for quick review alone/image display OFF.
- 10

In the above description, upon reception of an end instruction of the quick review display on the image display unit 28, an elapse of the minimum review time in which the quick review display continues for a minimum period of time is checked. However, no minimum review time may be set. In such case, in the flow chart in Fig. 3, step S137 of checking an elapse of the minimum review time is omitted.

15

Also, dedicated image display units for the through display and quick review display may be equipped, and may be turned on/off in correspondence with the setups of the image display ON/OFF switch and quick review ON/OFF switch, respectively. In this case as well, the function that can attain both confirmation of the sensed image and power savings can be implemented as in the description of the embodiment.

20  
25

In the above description, when the image display ON/OFF switch is ON, the quick review display immediately after image sensing is made irrespective of the setups of the quick review ON/OFF switch. However,

5 when the image display ON/OFF switch is ON, whether or not the quick review display is made may be determined in correspondence with the setups of the quick review ON/OFF switch.

Note that the recording media 200 and 210 are not

10 limited to memory cards such as PCMCIA cards, compact flash cards, or the like, hard disks, and the like, but may use micro DATs, magnetooptical disks, optical disks such as CD-Rs, CD-WRs, or the like, phase change optical disks such as DVDs, and the like.

15 Also, the recording media 200 and 210 may use hybrid media that integrate memory cards, hard disks, and the like. Furthermore, such hybrid media may include detachable media.

In the description of the embodiment, the

20 recording media 200 and 210 are independent from the image processing apparatus 100 and are arbitrarily connectable. One or both the recording media 200 and 210 may be permanently connected to the image processing apparatus 100.

An arbitrary number (one or a plurality) of image recording media 200 or 210 may be connectable to the image processing apparatus 100.

In the above description, the recording media 200  
5 and 210 are attached to the image processing apparatus. However, one or a combination of a plurality of recording media may be used.

To restate, according to the first embodiment, a sensed image can be confirmed without considerably  
10 reducing the operable time and the number of images that can be sensed.

(Second Embodiment)

The arrangement of an image sensing apparatus of the second embodiment shown in Fig. 7 is substantially  
15 the same as that of the first embodiment shown in Fig. 1 on the block diagrams, except that it has a compression mode switch 72. Since the operation of the second embodiment is different from the first embodiment, only differences from the first embodiment will be explained  
20 below.

Referring to Fig. 7, reference numeral 72 denotes the compression mode switch, which is used for selecting the compression ratio of JPEG compression or selecting a CCDRAW mode for directly recording a signal output from  
25 the image sensing element 14 and converted into a digital signal on a recording medium.

The JPEG compression mode includes, e.g., a normal mode and fine mode.

In the JPEG compression mode, image data, which is read out from the image sensing element 14 and is written in the memory 30 via the A/D converter 16, image processing circuit 20, and memory control circuit 22, is read out, and is compressed at the selected compression ratio by the compression/expansion circuit 32. After that, the compressed image data is recorded on the recording medium 200 or 210.

In the CCDRAW mode, image data, which is directly read out in units of lines in correspondence with the pixel formats of color filters of the image sensing element 14, and is written in the memory 30 via the A/D converter 16 and memory control circuit 22, is read out, and is recorded on the recording medium 200 or 210.

The operation of this embodiment will be explained below with reference to Figs. 8, 9, 10, 5, 11, 12, and 13.

Figs. 8, 9, and 10 are flow charts showing the main routine of an image processing apparatus 300 of this embodiment. The operation of the image processing apparatus 300 will be described below using Figs. 8, 9, and 10.

Upon power ON after battery exchange or the like, the system control circuit 50 initializes flags, control

variables, and the like (S501), and resets image display of the image display unit 28 to an OFF state (S502).

The system control circuit 50 checks the setting position of the mode dial 60. If the mode dial 60 is set 5 at a power OFF position (S503), the system control circuit 50 executes a predetermined end process (S505). More specifically, the system control circuit 50 changes the indications of the respective indication units to an end state, protects the image sensing unit by closing 10 the barrier of the protection means 102, records required parameters and setting values including flags, control variables, and the like, and the setting mode in the nonvolatile memory 56, cuts off unnecessary power supply to the respective units of the image processing 15 apparatus 300 including the image display unit 28 by the power supply control means 80, and so forth. After that, the flow returns to step S503.

On the other hand, if the mode dial 60 is set at an image sensing mode position (S503), the flow advances 20 to step S506.

If the mode dial is set at any of other mode positions (S503), the system control circuit 50 executes a process corresponding to the selected mode (S504), and the flow returns to step S503 upon completion of the 25 process.

- The system control circuit 50 checks using the power supply control means 80 if the remaining capacity and operation state of the power supply means 86 comprising batteries and the like pose any problem in  
5 the operation of the image processing apparatus 300 (S506). If a problem is found, a predetermined alert indication is made by means of an image or voice using the indication unit 54 (S508), and the flow then returns to step S503.
- 10 If no problem is found in the power supply means 86 (S506), the system control circuit 50 checks if the operation state of the recording medium 200 or 210 poses any problem in the operation of the image processing apparatus 300, in particular, recording/playback of  
15 image data to/from the recording medium (step S507). If any problem is found, a predetermined alert indication is made by means of an image or voice using the indication unit 54 (S508), and the flow then returns to step S503.
- 20 If no problem is found in the operation state of the recording medium 200 or 210 (S507), various setting states of the image display apparatus 300 are indicated by means of an image or voice using the indication unit 54 (S509). When the image display of the image display  
25 unit 28 is ON, various setting states of the image

display apparatus 300 are indicated by means of an image or voice also using the image display unit 28.

The system control circuit 50 checks the setting state of the quick review ON/OFF switch 68 (S510). If 5 the quick review mode is ON, the system control circuit 50 sets a quick review flag (S511); otherwise, it resets the quick review flag (S512).

Note that the status of the quick review flag is stored in an internal memory of the system control 10 circuit 50 or the memory 52.

Subsequently, the system control circuit 50 checks the setting state of the image display ON/OFF switch 66 (S513). If the image display mode is ON, the system control circuit 50 sets an image display flag (S514), 15 turns on the image display of the image display unit 28 (S515), and sets a through display state for displaying sensed image data in turn (S516). The flow then advances to step S519.

In the through display state, data written in turn 20 in the image display memory 24 via the image sensing element 14, A/D converter 16, image processing circuit 20, and memory control circuit 22 are displayed in turn 25 on the image display unit 28 via the memory control circuit 22 and D/A converter 26, thus implementing the electronic viewfinder function.

If the image display ON/OFF switch 66 is set in an image display OFF mode (S513), the system control circuit 50 resets the image display flag (S517), and turns off the image display of the image display unit 28 5 (S518). Then, the flow advances to step S519.

When the image display is OFF, image sensing is done using the optical viewfinder 104 without using the electronic viewfinder function implemented by the image display unit 28. In this case, the power consumption of 10 the image display unit 28, D/A converter 26, and the like, which consume large power, can be reduced.

Note that the status of the image display flag is stored in the internal memory of the system control circuit 50 or the memory 52.

15 The system control circuit 50 then checks the setup state of the compression mode switch 72 (S519). If the image compression mode is set in the CCDRAW mode, the system control circuit 50 sets a CCDRAW mode flag (S520), and the flow advances to step S531 (see Fig. 9).

20 In the CCDRAW mode, image data, which is directly read out in units of lines in correspondence with the pixel formats of color filters of the image sensing element 14, and is written in the memory 30 via the A/D converter 16 and memory control circuit 22, is read out, 25 and is recorded on the recording medium 200 or 210.

Since the CCDRAW mode can directly record an image signal captured by the image sensing element 14 on the recording medium, high-quality recording can be achieved but the image data size to be recorded becomes large.

5        If the compression mode switch 72 is set in the JPEG mode (S519), the system control circuit 50 sets a JPEG mode flag (S521), and the flow advances to step S531.

In the JPEG mode, image data, which is read out  
10 from the image sensing element 14 and is written in the memory 30 via the A/D converter 16, image processing circuit 20, and memory control circuit 20, is read out, and is compressed at the selected compression ratio by the compression/expansion circuit 32. After that, the  
15 compressed image data is recorded on the recording medium 200 or 210.

Since the JPEG mode can compress and record an image signal captured by the image sensing element 14 on the recording medium, more image signals can be recorded  
20 by reducing each image data size to be recorded. However, when image compression uses JPEG as irreversible compression, the image quality deteriorates in correspondence with the compression ratio.

Note that the status of the compression mode flag  
25 is stored in the internal memory of the system control circuit 50 or the memory 52.

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If the shutter switch SW1 has not been pressed yet (S531; see Fig. 9), the flow returns to step S503.

If the shutter switch SW1 has been pressed (S531), the system control circuit 50 checks the status of the  
5 image display flag stored in its internal memory or the memory 52 (S532). If the image display flag is set, the system control circuit 50 sets the display state of the image display unit 28 in a freeze display state (S533), and the flow then advances to step S534.

10 In the freeze display state, image data on the image display memory 24 is inhibited from being rewritten via the image sensing element 14, A/D converter 16, image processing circuit 20, and memory control circuit 22, and the latest written image data is  
15 displayed on the image display unit 28 via the memory control circuit 22 and D/A converter 26, thus displaying a frozen image on the electronic viewfinder.

If the image display flag is reset (S532), the flow advances to step S534.

20 The system control circuit 50 executes a distance measurement to adjust the focus of the photographing lens 10 on an object, and determines the aperture value and shutter speed by photometry (S534). In photometry, the electronic flash is set up if necessary. The details  
25 of the distance measurement/photometry process (S534) are the same as those that have already described in the

first embodiment using Fig. 4, and a detailed description thereof will be omitted.

- Upon completion of the distance measurement/photometry process (S534), the system
- 5 control circuit 50 checks the status of the image display flag stored in its internal memory or the memory 52 (S535). If the image display flag is set, the system control circuit 50 sets the display state of the image display unit 28 in a through display state (S536), and
- 10 the flow then advances to step S537. Note that the through display state in step S536 is the same operation state as that in step S516 (see Fig. 8).

If the shutter switch SW2 is not pressed (S537) and the shutter switch SW1 is canceled (S546), the flow

15 returns to step S503.

If the shutter switch SW2 has been pressed (S537), the system control circuit 50 checks the status of the image display flag stored in its internal memory or the memory 52 (S538). If the image display flag is set, the

20 system control circuit 50 sets the display state of the image display unit 28 in a fixed color display state (S539), and the flow then advances to step S561 (see Fig. 10).

In the fixed color display state, in place of

25 sensed image data, which has been written in the image display memory 24 via the image sensing element 14, A/D

converter 16, image processing circuit 20, and memory control circuit 22, substitute fixed-color image data is displayed on the image display unit 28 via the memory control circuit 22 and D/A converter 26, thus displaying 5 a fixed-color image on the electronic viewfinder 28.

If the image display flag is reset (S538), the flow advances to step S561.

The system control circuit 50 executes an image sensing process including an exposure process for

10 writing sensed image data in the memory 30 via the image sensing element 14, A/D converter 16, image processing circuit 20, and memory control circuit 22 or directly from the A/D converter via the memory control circuit 22, and a development process for reading out image data  
15 written in the memory 30 and executing various processes of the readout image data using the memory control circuit 22 and the image processing circuit 20 if required (S561).

The image sensing process (S561) will be explained 20 in detail later using Fig. 11.

Upon completion of the image sensing process (S561), the system control circuit 50 checks the status of the compression mode flag stored in its internal memory or the memory 52 (S562).

25 <When Compression Mode Flag = JPEG Mode>

If the status of the compression mode flag indicates the JPEG mode (S562), a display image process for reading out the image data written in the memory 30 by the image sensing process (S561), and transferring 5 that data to the image display memory 24 via the memory control circuit 22 is executed (S563).

The display image process (S563) will be explained in detail later with reference to Fig. 12.

The system controller 50 checks the status of the 10 image display flag stored in its internal memory or the memory 52 (S564). If the image display flag is set, the system control circuit 50 performs a quick review display (S565). In this case, the image display unit 28 always displays an image as an electronic viewfinder 15 even during image sensing, and a quick review display immediately after image sensing is also made.

If the image display flag is reset (S564), the system control circuit 50 checks the status of the quick review flag stored in its internal memory or the memory 20 52 (S565). If the quick review flag is set, the system control circuit 50 turns on the image display of the image display unit 28 (S566) to make a quick review display (S567).

In this case, even when image display of the image 25 display unit 28 is turned off to achieve power savings or to make image sensing using the optical viewfinder

104 without requiring any electronic viewfinder function, if the quick review function is set by the quick review switch 68, a sensed image can be automatically played back on the image display unit 28 immediately after

- 5 image sensing, thus providing a function that is convenient for power savings and confirmation of the sensed image.

If the image display flag is reset (S564) and the quick review flag is also reset (S565), the flow

- 10 advances to step S568 while the image display unit 28 is kept OFF. In this case, even after image sensing, the image display unit 28 is kept OFF, and no quick review display is made. This method is used when the sensed image need not be confirmed immediately after image  
15 sensing, and it is more important to attain power savings without using the electronic viewfinder function of the image display unit 28 like in a case wherein the user continues image sensing using the optical viewfinder 104.

- 20 The system control circuit 50 reads out sensed image data written in the memory 30, and executes various image processes using the memory control circuit 22 and the image processing circuit 20 if required, and a compression process that executes image compression  
25 corresponding to the selected mode using the compression/expansion circuit 32 (S568). The system

control circuit 50 then executes a recording process for writing image on the recording medium 200 or 210 (S569).

The compression process (S568) will be described in detail later using Fig. 13.

5        When the image display unit 28 is ON, a message such as "BUSY" or the like that indicates a write in progress is displayed on the image display unit 28 during a write of image data on the recording medium 200 or 210.

10      Furthermore, the indication unit 54 makes a recording medium write access indication by flickering an LED or the like in combination with the above message.

After the recording process (S569), the flow advances to step S540 (see Fig. 9).

15 <When Compression Mode Flag = CCDRAW mode>

If the status of the compression mode flag indicates the CCDRAW mode (S562), the system control circuit 50 checks the status of the image display flag stored in its internal memory or the memory 52 (S570).

20 If the image display flag is set, the system control circuit 50 sets the display state of the image display unit 28 in the fixed-color display state (S573), and the flow advances to step S574.

In this case, by setting the image display unit 28 25 in the fixed-color display state, the system control circuit 50 can display progress of recording in the

recording process (S575; to be described later) on the image display unit 28.

Since the recording process in the CCDRAW mode can directly record an image signal captured by the image sensing element 14 on the recording medium, high-quality recording can be achieved but the image data size to be recorded becomes large. For this reason, displaying progress of recording is convenient for the user.

If the image display flag is reset (S570), the system control circuit 50 checks the status of the quick review flag stored in its internal memory or the memory 52 (S571). If the quick review flag is set, the system control circuit 50 turns on the image display of the image display unit 28 (S572), and sets the display state 15 of the image display unit 28 in the fixed-color display state (S573). Then, the flow advances to step S574.

In this case as well, by setting the image display unit 28 in the fixed-color display state, the system control circuit 50 can display progress of recording in 20 the recording process (S575; to be described later) on the image display unit 28.

If the image display flag is reset (S570) and the quick review flag is also reset (S571), the flow advances to step S575 while the image display unit 28 is 25 kept OFF. In this case, the recording process (S575; to

be described later) is executed without displaying any recording progress state on the image display unit 28.

The system control circuit 50 reads out image data, which was directly read out in units of lines in  
5 correspondence with the pixel formats of the color filters of the image sensing element 14 and was written in the memory 30 via the A/D converter 16 and memory control circuit 22 in the image sensing process in step S561, and records it on the recording medium 200 or 210  
10 such as a memory card, compact flash card, or the like via the interface 90 or 94, and the connector 92 or 96 (S575), while displaying the progress of the recording process in step S575 on the image display unit 28 (S574).

When the image display unit 28 is ON, a message  
15 such as "BUSY" or the like that indicates a write in progress is displayed on the image display unit 28 during a write of image data on the recording medium 200 or 210.

Furthermore, the indication unit 54 makes a  
20 recording medium write access indication by flickering an LED or the like in combination with the above message.

This recording process in step S575 is executed until recording on the recording medium 200 or 210 is completed (S576) while updating the display of the  
25 recording progress state when the image display of the image display unit 28 is ON (S574).

Upon completion of the recording process (S576), the system control circuit 50 checks the status of the image display flag stored in its internal memory or the memory 52 (S577). If the image display flag is set, the  
5 flow advances to step S579.

If the image display flag is reset (S577), the system control circuit 50 checks the status of the quick review flag stored in its internal memory or the memory 52 (S578). If the quick review flag is set, the flow  
10 advances to step S579.

If the image display flag is reset (S577) and the quick review flag is also reset (S578), the flow advances to step S540 (see Fig. 9) while the image display unit 28 is kept OFF.

15 Upon completion of the display of the recording progress state on the image display unit 28 (S579), the system control circuit 50 executes a display image process for reading out image data, which was directly read out in units of lines in correspondence with the  
20 pixel formats of the color filters of the image sensing element 14 and was written in the memory 30 via the A/D converter 16 and memory control circuit 22 in the image sensing process in step S561, executing various processes of the readout data using the image processing circuit 20, and transferring the processed data to the image display memory 24 via the memory control circuit  
25

22 (S580). With this display image process, when the image display unit 28 is ready to playback a sensed image, the quick review display is made (S581).

The display image process (S580) will be explained  
5 in detail later with reference to Fig. 12.

As described above, according to this embodiment, immediately after the image sensing/recording process in the CCDRAW mode, the display image process is executed to automatically play back a sensed image on the image  
10 display unit 28. In addition, since the progress of recording is displayed during image data recording, a function that is convenient for confirmation of the sensed image and confirmation associated jobs can be provided.

15 Also, even when the image display of the image display unit 28 is turned off to achieve power savings or to make image sensing using the optical viewfinder 104 without requiring any electronic viewfinder function, if the quick review function is set by the quick review  
20 switch, a sensed image can be automatically played back on the image display unit 28 immediately after image sensing and recording, thus providing a function that is convenient for power savings and confirmation of a sensed image.

25 After the quick review display (S581), the flow advances to step S540.

Upon completion of a series of processes (S563 to S569) in the JPEG mode or a series of processes (S570 to S581) in the CCDRAW mode, if the shutter switch SW2 has been pressed (S540; see Fig. 9), the system control

- 5 circuit 50 checks the status of a continuous shot flag stored in its internal memory or the memory 52 (S541). If the continuous shot flag is set, the flow returns to step S561 to continuously make image sensing, and the next image sensing is done.
  - 10 If the continuous shot flag is not set (S541), the current process repeats itself until the shutter switch SW2 is released (S540).

As described above, according to this embodiment, in an operation setup state that makes the quick review display immediately after image sensing, if the shutter switch SW2 is kept pressed upon completion of the recording process (S569, S575), the quick review display on the image display unit 28 can continue until the shutter switch SW2 is released, and the user can carefully check the sensed image.

If the shutter switch SW2 is not pressed upon completion of a series of processes (S563 to S569) in the JPEG mode or a series of processes (S570 to S581) in the CCDRAW mode, or if the shutter switch SW2 is released after the sensed image is checked by holding down the shutter switch SW2 to continue the quick review

display (S540), the flow advances to step S543 after an elapse of a predetermined minimum review time (S542).

- As described above, according to this embodiment, since the quick review display on the image display unit 28 continues for a predetermined period of time, the user can reliably confirm the sensed image, and can be prevented from losing the next image sensing chance by continuing the quick review display for an unnecessarily long period of time.
- Note that the setting method of this minimum review time is not particularly limited. That is, the minimum review time may be set at a fixed value, may be arbitrarily set by the user, or may be arbitrarily set or selected by the user within a predetermined range.
- If the system control circuit 50 determines that the image display flag is set (S543), it sets the display state of the image display unit 28 in the through display state (S544), and the flow advances to step S546.
- In this case, after the sensed image is confirmed by the quick review display on the image display unit 28, the through display state can be set to display sensed image data in turn for the next image sensing.
- If the image display flag is reset (S543), the system control circuit 50 turns off the image display of

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the image display unit 28 (S545), and the flow advances to step S546.

In this case, after the sensed image is confirmed by the quick review display on the image display unit 28,

5 the function of the image display unit 28 is disabled to attain power savings, i.e., the consumption power of the image display unit 28, D/A converter 26, and the like that require large power can be reduced.

If the shutter switch SW1 has been pressed (S546),

10 the flow returns to step S537 and the system control circuit 50 prepares for the next image sensing.

On the other hand, if the shutter switch SW1 is released (S546), the system control circuit 50 ends a series of image sensing operations, and the flow returns

15 to step S503.

Fig. 11 is a flow chart showing the image sensing process in step S561 in Fig. 10 in detail. The system control circuit 50 opens the shutter 12 having the stop function using the exposure control means 40 in

20 accordance with photometry data stored in its internal memory or the memory 52 to expose the image sensing element 14 (S601, S602).

The system control circuit 50 checks based on the flash flag if the electronic flash 48 is required (S603).

25 If the electronic flash 48 is required, the system

control circuit 50 controls the electronic flash to emit light (S604).

The system control circuit 50 waits for completion of exposure of the image sensing element 14 in

- 5 accordance with the photometry data (S605), closes the shutter 12 (S606), and reads a charge signal from the image sensing element 14 and writes sensed image data in the memory 30 via the A/D converter 16, image processing circuit 20, and memory control circuit 22 or directly  
10 from the A/D converter 16 via the memory control circuit 22 (S607). Upon completion of a series of processes, the image sensing process routine in step S561 ends.

Fig. 12 is a flow chart showing the display image process in step S563 or S580 in Fig. 10 in detail.

- 15 If a frame process is required in correspondence with the selected image sensing mode (S701), the system control circuit 50 reads out image data written in the memory 30, executes a vertical addition process (S702) and color process (S703) in turn using the memory  
20 control circuit 22 and image processing apparatus 20 if required, and writes the processed image data in the memory 30.

The system control circuit 50 reads out image data from the memory 30, and transfers display image data to the image display memory 24 via the memory control circuit 22 (S704).

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Upon completion of a series of processes, the display image process routine (S63 or S580) ends.

Fig. 13 is a flow chart showing the compression process in step S568 in Fig. 10 in detail.

- 5        The system control circuit 50 executes a pixel squaring process for reading out sensed image data written in the memory 30, and converting the vertical-to-horizontal pixel ratio of the image sensing element to 1 : 1 by interpolation using the memory  
10      control circuit 22 and image processing circuit 20 if required (S801). The system control circuit 50 then writes the processed image data in the memory 30.

The system control circuit 50 reads out image data written in the memory 30, and executes image compression  
15      corresponding to the selected mode using the compression/expansion circuit 32 (S802).

Upon completion of image compression, the compression process routine in step S568 ends.

The second embodiment of the present invention has  
20      been explained using Fig. 7.

In the description of the above embodiment, the compression modes include two different modes, i.e., the JPEG mode and CCDRAW mode. However, the combination of compression modes is not particularly limited as long as  
25      an image mode corresponding to the input display signal format of the image display unit 28 is combined with an

image mode corresponding to the output signal format of the image sensing element 14.

In this case as well, when the image mode corresponding to the input display signal format of the 5 image display unit 28 is selected, recording can be done while quickly playing back and confirming a sensed image immediately after image sensing. On the other hand, when the image mode corresponding to the output signal format of the image sensing element 14 is selected, recording 10 that utilizes image information of the image sensing element 14 can be done, and a sensed image can be played back and confirmed at the same time. The user can select one of the aforementioned image modes in correspondence with the situation of the image sensing location and the 15 image sensing purpose and can take an image.

When the image display unit 28 is turned off, all building components of the image display unit 28 are turned off, but only some building components of the image display unit 28 may be turned off. In such case, a 20 function that can attain both confirmation of the sensed image and power savings can be implemented. For example, when the image display unit 28 is comprised of an LCD and backlight, the backlight alone may be turned off while the LCD is kept ON, thus implementing the function 25 that can attain both confirmation of the sensed image

and power savings as in the description of the embodiment.

The image display ON/OFF switch 66 and quick review ON/OFF switch 68 are independently equipped.

- 5 However, the image display ON/OFF switch 66 and quick review ON/OFF switch 68 may be combined into a single, common switch, and that switch may have three positions, i.e., image display ON/image display ON for quick review alone/image display OFF.

- 10 In the above description, upon reception of an end instruction of the quick review display on the image display unit 28, an elapse of the minimum review time in which the quick review display continues for a minimum period of time is checked. However, no minimum review  
15 time may be set. In such case, in the flow chart in Fig. 9, step S542 of checking an elapse of the minimum review time is omitted.

- Also, dedicated image display units for the through display and quick review display may be equipped,  
20 and may be turned on/off in correspondence with the setups of the image display ON/OFF switch and quick review ON/OFF switch, respectively. In this case as well, the function that can attain both confirmation of the sensed image and power savings can be implemented as in  
25 the description of the embodiment.

In the above description, when the image display ON/OFF switch is ON, the quick review display immediately after image sensing is made irrespective of the setups of the quick review ON/OFF switch. However,

5 when the image display ON/OFF switch is ON, whether or not the quick review display is made may be determined in correspondence with the setups of the quick review ON/OFF switch.

Note that the recording media 200 and 210 are not

10 limited to memory cards such as PCMCIA cards, compact flash cards, or the like, hard disks, and the like, but may use micro DATs, magnetooptical disks, optical disks such as CD-Rs, CD-WRs, or the like, phase change optical disks such as DVDs, and the like.

15 Also, the recording media 200 and 210 may use hybrid media that integrate memory cards, hard disks, and the like. Furthermore, such hybrid media may include detachable media.

In the description of the embodiment, the

20 recording media 200 and 210 are independent from the image processing apparatus 300 and are arbitrarily connectable. One or both the recording media 200 and 210 may be permanently connected to the image processing apparatus 300.

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An arbitrary number (one or a plurality) of image recording media 200 or 210 may be connectable to the image processing apparatus 300.

In the above description, the recording media 200  
5 and 210 are attached to the image processing apparatus. However, one or a combination of a plurality of recording media may be used.

To restate, according to the second embodiment,  
whether a sensed image is directly played back and  
10 displayed immediately after image sensing and is then recorded, or a sensed image is recorded by utilizing its image quality, and then is played back and displayed can be selected in correspondence with each image sensing situation, purpose, and the like.

15 The present invention is not limited to the above embodiments and various changes and modifications can be made within the spirit and scope of the present invention. Therefore, to apprise the public of the scope of the present invention the following claims are made.

WHAT IS CLAIMED IS:

1. An image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:
  - 5 image sensing means, memory means, control means, and display means,

wherein said control means stores an image signal output from said image sensing means in said memory means while said display means is disabled,
  - 10 enables said display means immediately after the storage, and

plays back and displays the image signal stored in said memory means on said display means.
  - 15 2. The apparatus according to claim 1, wherein said display means comprises a liquid crystal display, and is disabled by turning off only a backlight.
  3. An image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:
    - 20 image sensing means, memory means, control means, and display means,

wherein said control means stores an image signal output from said image sensing means in said memory means while said display means is disabled,
    - 25 enables said display means immediately after the storage,

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plays back and displays the image signal stored in said memory means on said display means for a predetermined period of time, and

then disables said display means.

5 4. The apparatus according to claim 3, wherein said display means comprises a liquid crystal display, and is disabled by turning off only a backlight.

5. An image processing apparatus for recording, and playing back and displaying a sensed still image and/or 10 moving image on a recording medium, comprising:

image sensing means, memory means, control means, display means, display mode setting means, and sense-and-play mode setting means,

wherein when said display mode setting means is 15 set to disable said display means, and

said sense-and-play mode setting means is set to play back an image immediately after image sensing,

20 said control means stores an image signal output from said image sensing means in said memory means while said display means is disabled,

enables said display means immediately after the storage, and

plays back and displays the image signal stored in said memory means on said display means.

6. The apparatus according to claim 5, wherein said display means comprises a liquid crystal display, and is disabled by turning off only a backlight.
7. An image processing apparatus for recording, and  
5 playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:
  - image sensing means, memory means, control means, display means, display mode setting means, sense-and-play mode setting means, and image sensing
  - 10 instruction means,
    - wherein when said display mode setting means is set to disable said display means, and
      - said sense-and-play mode setting means is set to play back an image immediately after image sensing,
      - 15 if said image sensing instruction means instructs start of image sensing,
        - said control means stores an image signal output from said image sensing means in said memory means while said display means is disabled, and
      - 20 enables said display means immediately after the storage, and
        - if said image sensing instruction means instructs end of image sensing,
          - said control means disables said display means.

8. The apparatus according to claim 7, wherein said display means comprises a liquid crystal display, and is disabled by turning off only a backlight.

9. An image processing apparatus for recording, and  
5 playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:

image sensing means, memory means, recording means, control means, display means, display mode setting means, and sense-and-play mode setting means,

10 wherein when said display mode setting means is set to disable said display means, and

said sense-and-play mode setting means is set to play back an image immediately after image sensing,

15 said control means stores an image signal output from said image sensing means in said memory means while said display means is disabled,

enables said display means immediately after the storage,

20 plays back and displays the image signal stored in said memory means, and

transfers the image signal stored in said memory means to record the image signal on said recording means.

10. The apparatus according to claim 9, wherein said display means comprises a liquid crystal display, and is  
25 disabled by turning off only a backlight.

11. An image processing apparatus for recording, and  
playing back and displaying a sensed still image and/or  
moving image on a recording medium, comprising:

image sensing means, memory means, recording means,  
5 control means, display means, display mode setting means,  
sense-and-play mode setting means, and image sensing  
instruction means,

wherein when said display mode setting means is  
set to disable said display means, and

10 said sense-and-play mode setting means is set to  
play back an image immediately after image sensing,

if said image sensing instruction means instructs  
start of image sensing,

15 said control means stores an image signal output  
from said image sensing means in said memory means while  
said display means is disabled,

enables said display means immediately after the  
storage,

20 plays back and displays the image signal stored in  
said memory means, and

transfers the image signal stored in said memory  
means to record the image signal on said recording means,  
and

25 if said image sensing instruction means instructs  
end of image sensing,

said control means disables said display means.

12. The apparatus according to claim 11, wherein said display means comprises a liquid crystal display, and is disabled by turning off only a backlight.

13. A control method of an image processing apparatus  
5 for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of:

storing an image signal output from image sensing means in a memory while display means is disabled;

10 enabling said display means immediately after the storage; and

playing back and displaying the image signal stored in said memory means on said display means.

14. A control method of an image processing apparatus  
15 for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of:

storing an image signal output from image sensing means in a memory while display means is disabled;

20 enabling said display means immediately after the storage;

playing back and displaying the image signal stored in said memory means on said display means for a predetermined period of time; and

25 then disabling said display means.

15. A control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of:

5           when display mode setting means is set to disable  
display means and

sense-and-play mode setting means is set to play back an image immediately after image sensing.

storing an image signal output from image sensing  
means in a memory while said display means is disabled;  
enabling said display means immediately after the  
storage; and

playing back and displaying the image signal stored in said memory means on said display means.

15 16. A control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising the steps of:

when display mode setting means is set to disable  
20 display means and

sense-and-play mode setting means is set to play back an image immediately after image sensing.

if image sensing instruction means instructs start  
of image sensing,

25 storing an image signal output from image sensing means in a memory while said display means is disabled;

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enabling said display means immediately after the storage; and

playing back and displaying the image signal stored in said memory means on said display means, and  
5 if said image sensing instruction means instructs end of image sensing,  
disabling said display means.

17. A control method of an image processing apparatus for recording, and playing back and displaying a sensed  
10 still image and/or moving image on a recording medium, comprising the steps of:

when display mode setting means is set to disable display means and

15 sense-and-play mode setting means is set to play back an image immediately after image sensing,  
storing an image signal output from image sensing means in a memory while said display means is disabled;  
enabling said display means immediately after the storage;

20 playing back and displaying the image signal stored in said memory means on said display means; and  
transferring the image signal stored in said memory means to record the image signal on recording means.

25 18. A control method of an image processing apparatus for recording, and playing back and displaying a sensed

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still image and/or moving image on a recording medium,  
comprising the steps of:

when display mode setting means is set to disable  
display means and

5       sense-and-play mode setting means is set to play  
back an image immediately after image sensing,

if image sensing instruction means instructs start  
of image sensing,

10      storing an image signal output from image sensing  
means in a memory while said display means is disabled;

enabling said display means immediately after the  
storage;

playing back and displaying the image signal  
stored in said memory means on said display means; and

15      transferring the image signal stored in said  
memory means to record the image signal in recording  
means, and

if said image sensing instruction means instructs  
end of image sensing,

20      disabling said display means.

19. A storage medium storing a program for  
implementing a control method of an image processing  
apparatus of claim 13.

20. A storage medium storing a program for  
25 implementing a control method of an image processing  
apparatus of claim 14.

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21. A storage medium storing a program for  
implementing a control method of an image processing  
apparatus of claim 15.
22. A storage medium storing a program for  
5 implementing a control method of an image processing  
apparatus of claim 16.
23. A storage medium storing a program for  
implementing a control method of an image processing  
apparatus of claim 17.
- 10 24. A storage medium storing a program for  
implementing a control method of an image processing  
apparatus of claim 18.
25. An image processing apparatus for recording, and  
15 playing back and displaying a sensed still image and/or  
moving image on a recording medium, comprising:  
image sensing means, recording means, and display  
means, and  
said apparatus having:  
a first recording mode for displaying an image  
20 signal output from said image sensing means on said  
display means, and then recording the image signal in  
said recording means in an image signal display format;  
and  
a second recording mode for recording an image  
25 output from said image sensing means in said recording

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- means in an image signal sense format, and then displaying the image signal on said display means.
26. An image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:
- 5       image sensing means, signal processing means, recording means, and display means,
- said image processing apparatus having:
- 10      a first signal format corresponding to an input image signal of said display means; and
- a second signal format corresponding to an output image signal of said image sensing means, and
- said apparatus further having:
- 15      a first recording mode for displaying an image signal output from said image sensing means on said display means by converting the image signal into the first signal format by said signal processing means, and then recording the image signal in said recording means; and
- 20      a second recording mode for recording an image output from said image sensing means in said recording means in the second signal format, and then displaying the image signal on said display means by converting the image signal into the first signal format by said signal processing means.
- 25

27. An image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:

- image sensing means, signal processing means,
- 5 compression means, recording means, and display means,
- said image processing apparatus having:
  - a first signal format corresponding to an input image signal of said display means; and
  - a second signal format corresponding to an output image signal of said image sensing means, and
  - 10    said apparatus further having:
    - a first recording mode for displaying an image signal output from said image sensing means on said display means by converting the image signal into the first signal format by said signal processing means, and
    - 15        then compressing the converted image signal by said compression means and recording the compressed signal in said recording means; and
    - a second recording mode for recording an image signal output from said image sensing means in the second signal format,
    - 20        converting the recorded image signal into the first signal format by said signal processing means, and
    - displaying the converted signal on said display means.

28. A control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:

5       the first recording mode step of displaying an image signal output from image sensing means on display means, and then recording the image signal in recording means in an image signal display format;

10      the second recording mode step of recording an image signal output from said image sensing means in said recording means in an image signal sense format, and then displaying the image signal on said display means; and

15      the step of selecting one of the first and second recording mode steps.

29. A control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:

20      the first recording mode step of displaying an image signal output from image sensing means on display means by converting the image signal into a first signal format corresponding to an input image signal of said display means, and recording the image signal in  
25      recording means;

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- the second recording mode step of recording an image signal output from said image sensing means in said recording means in a second signal format corresponding to an output image signal of said image sensing means, and then displaying the image signal on said display means by converting the image signal into the first signal format; and
- 5        the step of selecting one of the first and second recording mode steps.
- 10      30. A control method of an image processing apparatus for recording, and playing back and displaying a sensed still image and/or moving image on a recording medium, comprising:
- 15        the first recording mode step of displaying an image signal output from image sensing means on display means by converting the image signal into a first signal format corresponding to an input image signal of said display means, compressing the converted image signal, and recording the compressed image signal in recording means;
- 20        the second recording mode step of recording an image signal output from said image sensing means in said recording means in a second signal format corresponding to an output image signal of said image sensing means, converting the recorded image signal into

the first signal format, and displaying the converted image signal on said display means; and

the step of selecting one of the first and second recording mode steps.

5 31. A storage medium storing a program for implementing a control method of an image processing apparatus of claim 28.

32. A storage medium storing a program for implementing a control method of an image processing 10 apparatus of claim 29.

33. A storage medium storing a program for implementing a control method of an image processing apparatus of claim 30.

ABSTRACT OF THE DISCLOSURE

This invention has as its object to provide a control method of an image processing apparatus which allows confirmation of the sensed image without 5 considerably reducing the operable time of a camera and the number of images that can be sensed. To achieve this object, even when an image display mode is disabled, if a quick review mode is enabled, an image is recorded on a memory immediately after image sensing, a display unit 10 is enabled, and the image stored in the memory is played back and displayed.

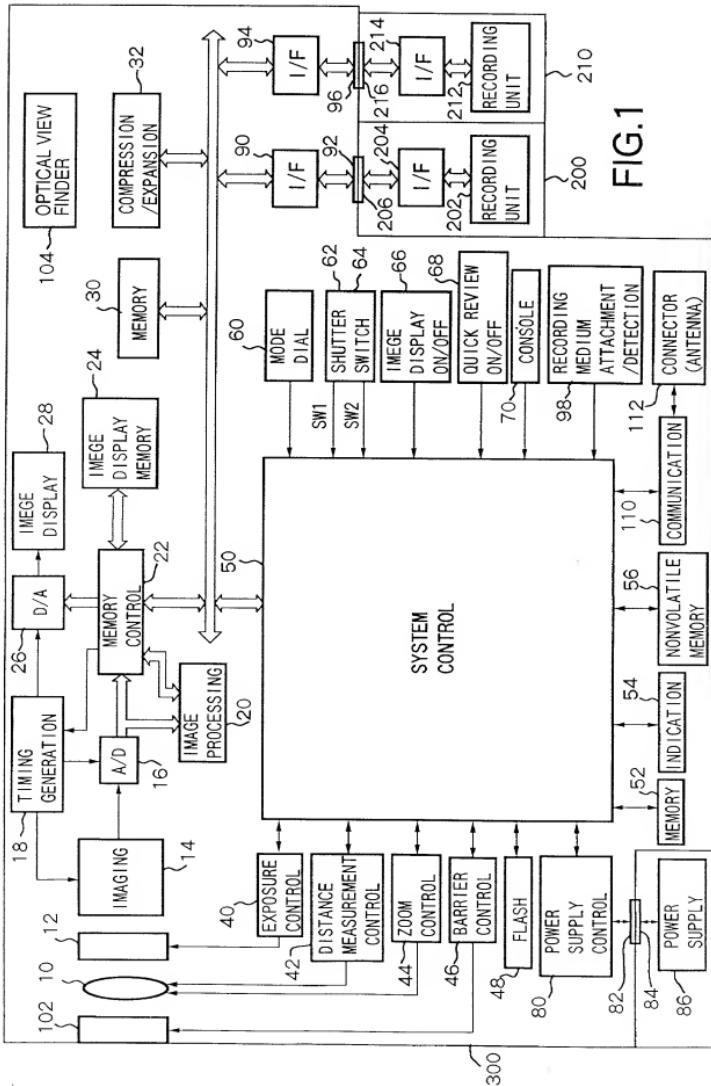


FIG.2

## FLOW CHART OF MAIN ROUTINE

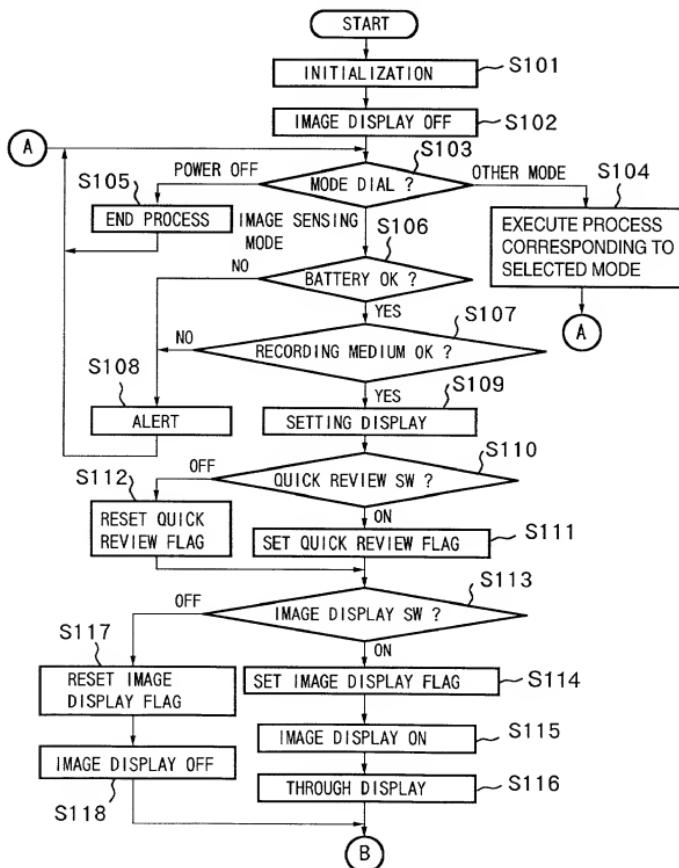


FIG.3

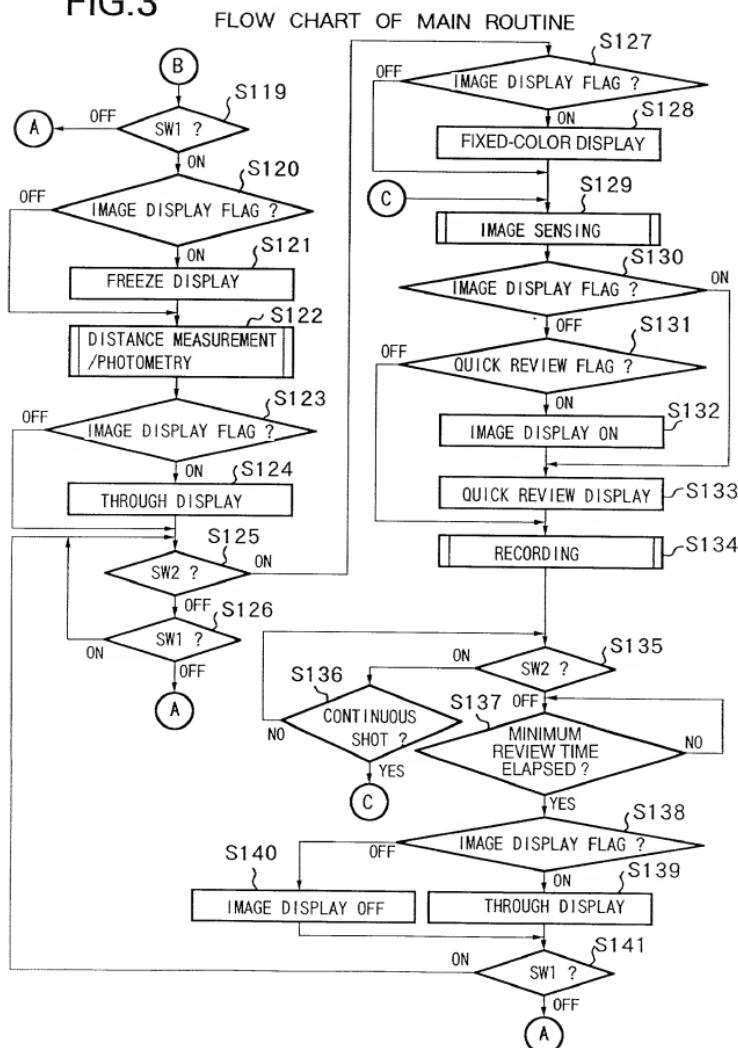
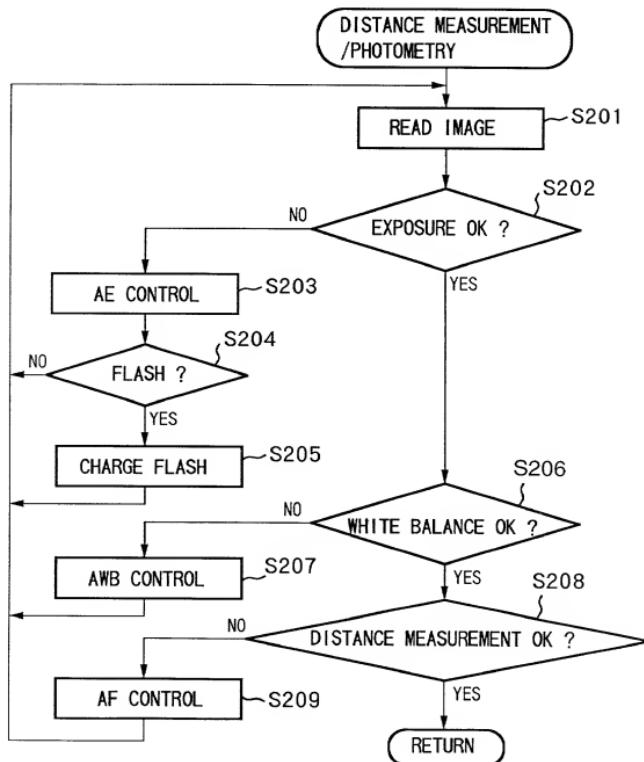


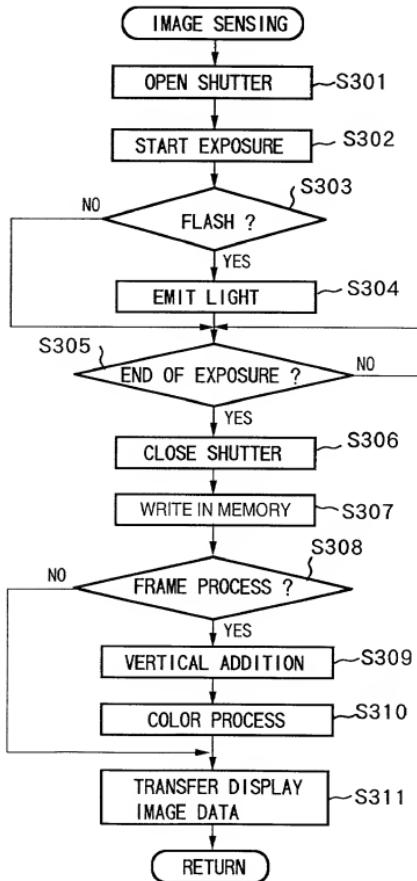
FIG.4

FLOW CHART SHOWING DISTANCE  
MEASUREMENT/PHOTOMETRY IN DETAIL



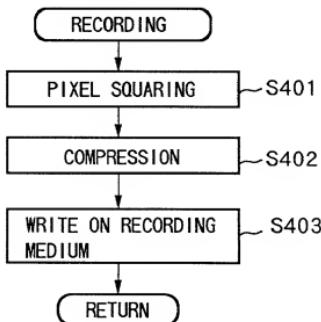
**FIG.5**

FLOW CHART SHOWING IMAGE SENSING  
PROCESS IN DETAIL



**FIG.6**

FLOW CHART SHOWING RECORDING  
PROCESS IN DETAIL



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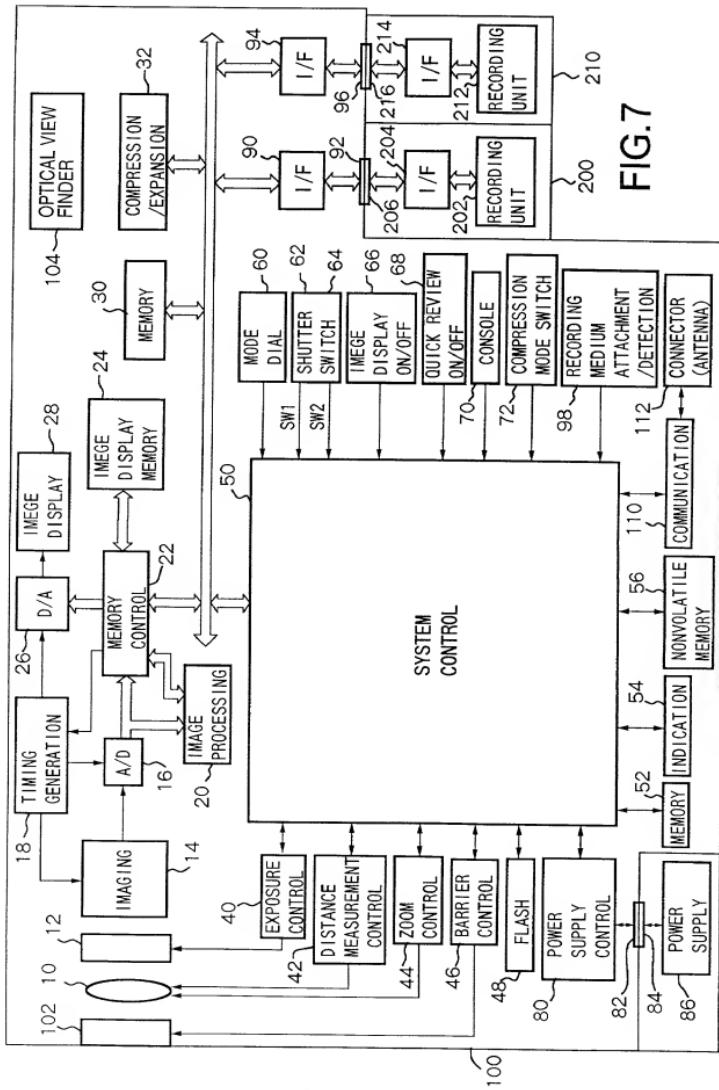


FIG. 7

## FLOW CHART SHOWING MAIN ROUTINE

FIG.8

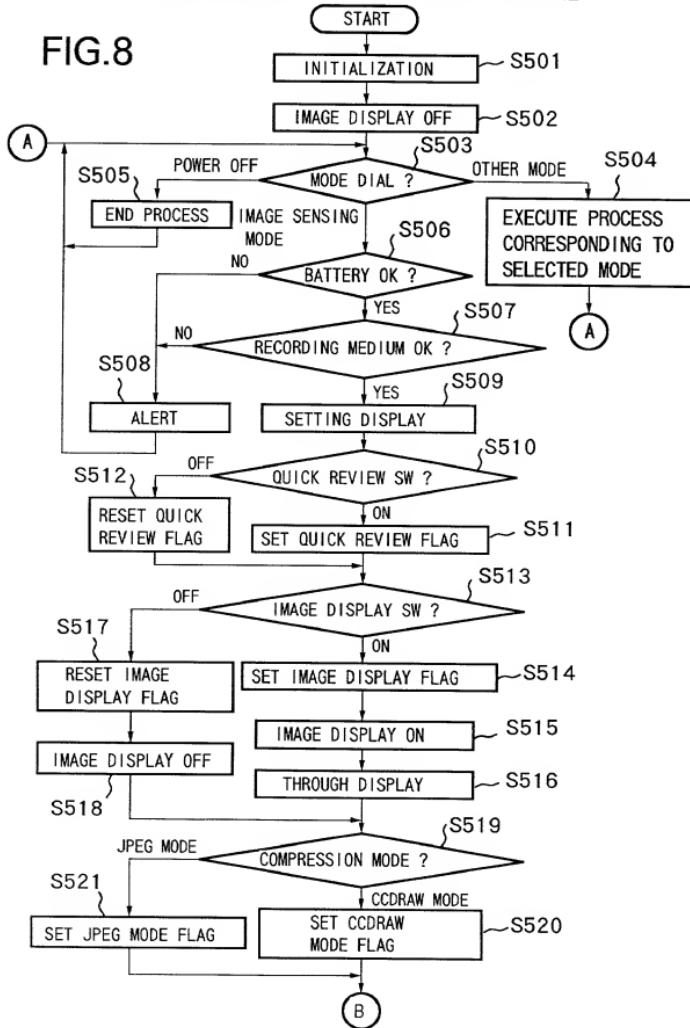


FIG.9

## FLOW CHART SHOWING MAIN ROUTINE

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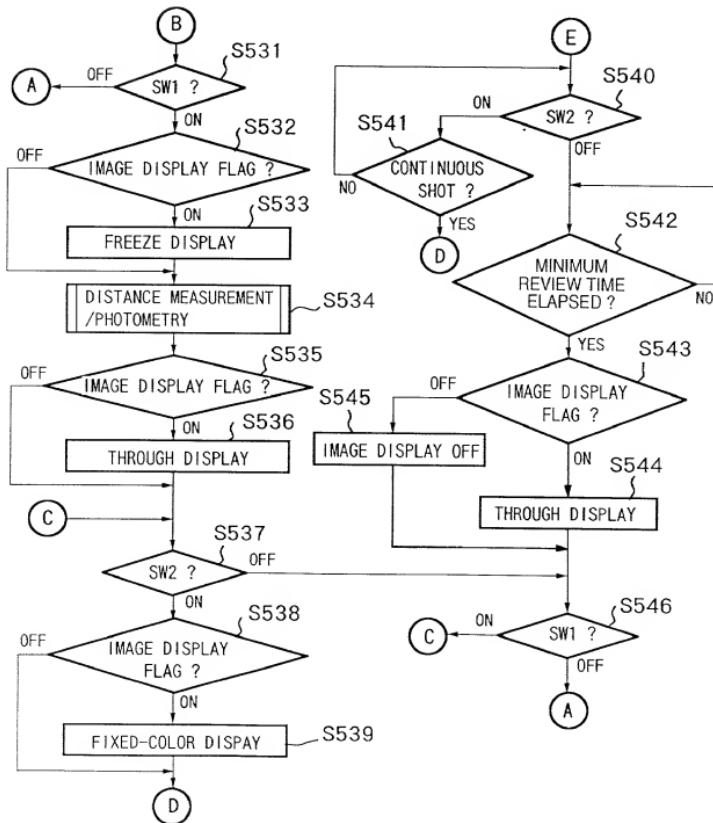
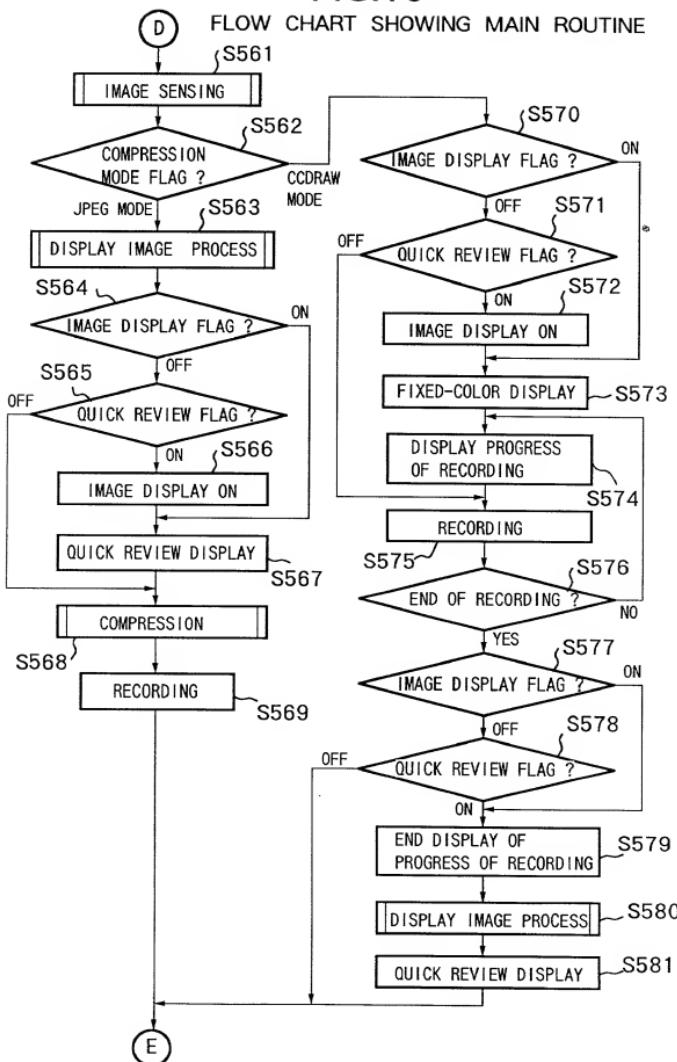
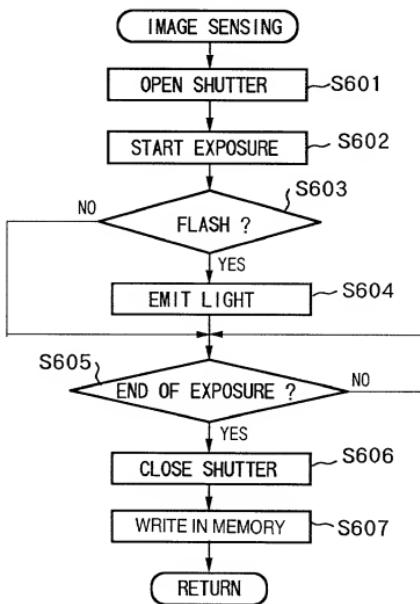


FIG.10



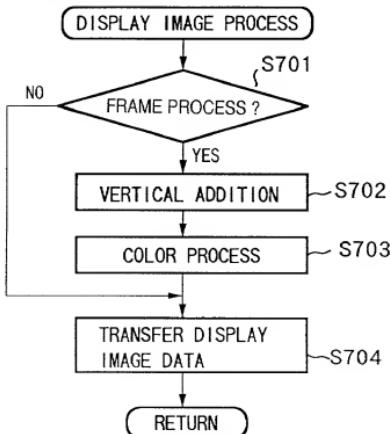
**FIG.11**

FLOW CHART SHOWING IMAGE SENSING  
PROCESS IN DETAIL

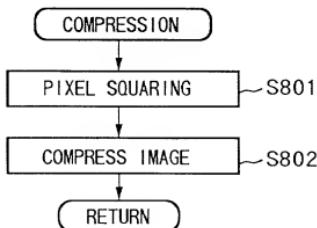


**FIG.12**

FLOW CHART SHOWING DISPLAY IMAGE PROCESS IN DETAIL

**FIG.13**

FLOW CHART SHOWING COMPRESSION PROCESS IN DETAIL



Docket No. \_\_\_\_\_

**COMBINED DECLARATION AND POWER OF ATTORNEY FOR  
ORIGINAL, DESIGN, NATIONAL STAGE OF PCT, SUPPLEMENTAL,  
DIVISIONAL, CONTINUATION OR CONTINUATION-IN-PART APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**IMAGE PROCESSING APPARATUS, ITS CONTROL METHOD, AND STORAGE MEDIUM**

the specification of which

a. [X] is attached hereto

b. [ ] was filed on \_\_\_\_\_ as application Serial No. \_\_\_\_\_ and  
was amended on \_\_\_\_\_ (if applicable).

**PCT FILED APPLICATION ENTERING NATIONAL STAGE**

C. [ ] was described and claimed in International Application No. \_\_\_\_\_ filed on  
\_\_\_\_\_ and as amended on \_\_\_\_\_ (if any).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby specify the following as the correspondence address to which all communications about this application are to be directed:

SEND CORRESPONDENCE TO: MORGAN & FINNEGAN, L.L.P.  
345 Park Avenue  
New York, N.Y. 10154

DIRECT TELEPHONE CALLS TO: MICHAEL M. MURRAY  
(212) 758-4800

[X] I hereby claim foreign priority benefits under Title 35, United States Code § 119 (a)-(d) or under § 365(b) of any foreign application(s) for patent or inventor's certificate or under § 365(a) of any PCT international application(s) designating at least one country other than the U.S. listed below and also have identified below such foreign application(s) for patent or inventor's certificate or such PCT international application(s) filed by me on the same subject matter having a filing date within twelve (12) months before that of the application on which priority is claimed:

[X] The attached 35 U.S.C. § 119 claim for priority for the application(s) listed below forms a part of this declaration.

<u>Country/PCT</u>	<u>Application Number</u>	<u>Date of filing (day,month,yr)</u>	<u>Date of issue (day,month,yr)</u>	<u>Priority Claimed</u>
JAPAN	10-059612	11/03/1998		[X] YES [ ] NO
JAPAN	10-059613	11/03/1998		[X] YES [ ] NO
				[ ] YES [ ] NO

[ ] I hereby claim the benefit under 35 U.S.C. § 119(e) of any U.S. provisional application(s) listed below.

Provisional Application No.

Date of filing (day, month, yr)

ADDITIONAL STATEMENTS FOR DIVISIONAL, CONTINUATION OR CONTINUATION-IN-PART  
OR PCT INTERNATIONAL APPLICATION(S) DESIGNATING THE U.S.)

I hereby claim the benefit under Title 35, United States Code § 120 of any United States application(s) or under § 365(c) of any PCT international application(s) designating the U.S. listed below.

<u>US/PCT Application Serial No.</u>	<u>Filing Date,</u>	<u>Status (patented, pending, abandoned)/ U.S. application no. assigned (For PCT)</u>
<u>US/PCT Application Serial No.</u>	<u>Filing Date,</u>	<u>Status (patented, pending, abandoned)/ U.S. application no. assigned (For PCT)</u>

[ ] In this continuation-in-part application, insofar as the subject matter of any of the claims of this application is not disclosed in the above listed prior United States or PCT international application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or Imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following attorneys and/or agents with full power of substitution and revocation, to prosecute this application, to receive the patent, and to transact all business in the Patent and Trademark Office connected therewith: John A. Diaz (Reg. No. 19,550), John C. Vassil (Reg. No. 19,098), Alfred P. Ewert (Reg. No. 19,887), David H. Pfeffer, P.C. (Reg. No. 19,825), Harry C. Marcus (Reg. No. 22,390), Robert E. Paulson (Reg. No. 21,046), Stephen R. Smith (Reg. No. 22,615), Kurt E. Richter (Reg. No. 24,052), J. Robert Dailey (Reg. No. 27,434), Eugene Moroz (Reg. No. 25,237), John F. Sweeney (Reg. No. 27,471), Arnold I. Rady (Reg. No. 26,601), Christopher A. Hughes (Reg. No. 26,914), William S. Feiler (Reg. No. 26,728), Joseph A. Calvaruso (Reg. No. 28,287), James W. Gould (Reg. No. 28,859), Richard C. Komson (Reg. No. 27,913), Israel Blum (Reg. No. 26,710), Bartholomew Verdirame (Reg. No. 28,483), Maria C. H. Lin (Reg. No. 29,323), Joseph A. DeGirolamo (Reg. No. 28,595), Michael A.

Nicodema (Ref. No. 33,199), Michael P. Dougherty (Ref. No. 32,730), Seth J. Altas (Reg. No. 32,454), Andrew M. Riddles (Reg. No. 31,657), Bruce D. DeRenzi (Reg. No. 33,676), Michael M. Murray (Reg. No. 32,537) and Mark J. Abate (Reg. No. 32,527); Alfred L. Haffner, Jr. (Reg. No. 18,919), Harold Haidt (Reg. No. 17,509), John T. Gallagher (Reg. No. 35,516), Steven F. Meyer (Reg. No. 35,613); Kenneth H. Sonnenfeld (Reg. No. 33,285), Edward A. Pennington (Reg. No. 32,588), Michael S. Marcus (Reg. No. 31,727) and John E. Hoel (Reg. No. 26,279) of Morgan & Finnegan, L.L.P., whose address is: 345 Park Avenue, New York, New York 10154.

I hereby authorize the U.S. attorneys and/or agents named hereinabove to accept and follow instructions from \_\_\_\_\_ as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and/or agents and me. In the event of a change in the person(s) from whom instructions may be taken I will so notify the U.S. attorneys and/or agents named hereinabove.

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Inventor's signature\* Yoichi Yamagishi date March 2, 1999

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Inventor's signature\* Taku Yamagami date March 2, 1999

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Citizenship Japan

c/o CANON KABUSHIKI KAISHA

Post Office Address 30-2, Shimomaruko 3-chome, Ohta-ku, Tokyo, Japan

ATTACHED IS ADDED PAGE TO COMBINED DECLARATION AND POWER OF ATTORNEY FOR SIGNATURE BY THIRD AND SUBSEQUENT INVENTORS FORM.

\* Before signing this declaration, each person signing must:

1. Review the declaration and verify the correctness of all information therein; and
2. Review the specification and the claims, including any amendments made to the claims.

After the declaration is signed, the specification and claims are not to be altered.

To the inventor(s):

The following are cited in or pertinent to the declaration attached to the accompanying application:

Title 37, Code of Federal Regulation, § 1.56

Duty to disclose information material to patentability.

(a) A patent by its very nature is affect with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is canceled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is canceled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in patent was cited by the Office or submitted to the Office in the manner prescribed by §§1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

- (1) prior art cited in search reports of a foreign patent office in a counterpart application, and
- (2) the closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.

Title 35, U.S. Code § 101

Inventions patentable

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

Title 35 U.S. Code § 102

Conditions for patentability; novelty and loss of right to patent

A person shall be entitled to a patent unless –

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for patent,
- (b) the invention was patented or described in a printed publication in this or foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States, or

- (c) he has abandoned the invention, or
- (d) the invention was first patented or caused to be patented, or was the subject of an inventor's certificate, by the applicant or his legal representatives or assigns in a foreign country prior to the date of the application for patent in this country on an application for patent or inventor's certificate filed more than twelve months before the filing of the application in the United States, or
- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent, or
- (f) he did not himself invent the subject matter sought to be patented, or
- (g) before the applicant's invention thereof the invention was made in this country by another had not abandoned, suppressed, or concealed it. In determining priority of invention there shall be considered not only the respective dates of conception and reduction to practice of the invention, but also the reasonable diligence of one who was first to conceive and last to reduce to practice, from a time prior to conception by the other ...

#### Title 35, U.S. Code § 103

##### Conditions for patentability; non-obvious subject matter

A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Subject matter developed by another person, which qualifies as prior art only under subsection (f) or (g) of section 102 of this title, shall not preclude patentability under this section where the subject matter and the claimed invention were, at the time the invention was made, owned by the same person or subject to an obligation of assignment to the same person.

#### Title 35, U.S. Code § 112 (in part)

##### Specification

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise and exact terms also enable any person skilled in the art to which it pertains, or with which it is mostly nearly connected, to make and use the same, and shall set forth the best mode contemplated by the inventor of carrying out his invention.

#### Title 35, U.S. Code § 119

##### Benefit of earlier filing date in foreign country; right of priority

An application for patent for an invention filed in this country by any person who has, or whose legal representatives or assigns have, previously regularly filed an application for a patent for the same invention in a foreign country which affords similar privileges in the case of applications filed in the United States or to citizens of the United States, shall have the same effect as the same application would have if filed in this country on the date on which the application for patent for the same invention was first filed in such foreign country, if the application in this country is filed within twelve months from the earliest date on which such

foreign application was filed; but no patent shall be granted on any application for patent for an invention which had been patented or described in a printed publication in any country more than one year before the date of the actual filing of the application in this country, or which had been in public use or on sale in this country more than one year prior to such filing.

Title 35, U.S. Code § 120

Benefit or earlier filing date in the United States

An application for patent for an invention disclosed in the manner provided by the first paragraph of section 112 of this title in an application previously filed in the United States, or as provided by section 363 of this title, which is filed by an inventor or inventors named in the previously filed application shall have the same effect, as to such invention, as though filed on the date of the prior application, if filed before the patenting or abandonment of or termination of proceedings on the first application or an application similarly entitled to the benefit of the filing date of the first application and if it contains or is amended to contain a specific reference to the earlier filed application.

Please read carefully before signing the Declaration attached to the accompanying Application.

If you have any questions, please contact Morgan & Finnegan, L.L.P.

FORM:COMB-DEC.NY  
Rev. 5/21/98